

FIG. 1

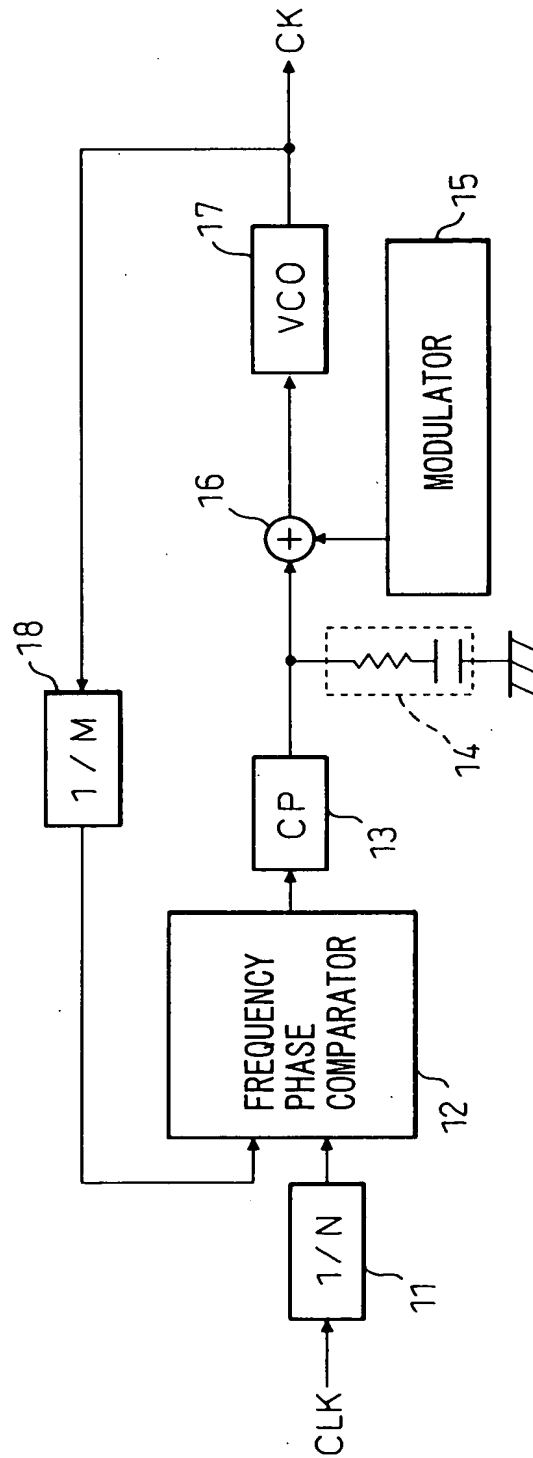


FIG.2

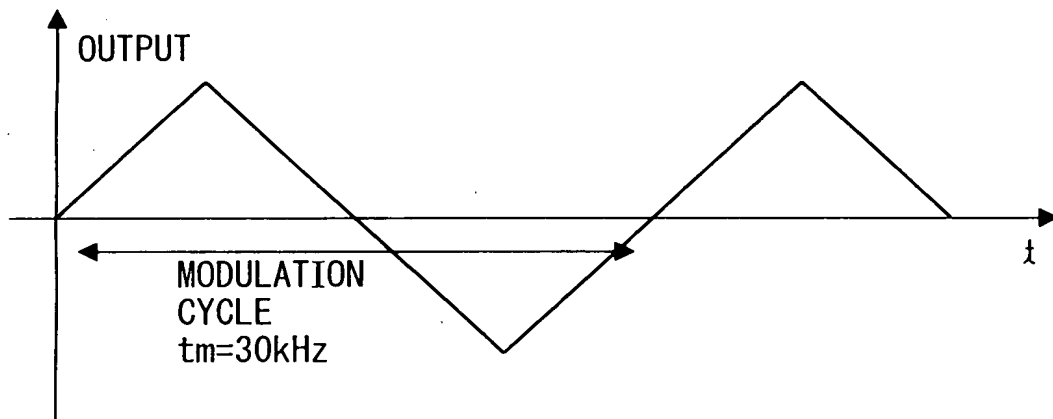


FIG.3

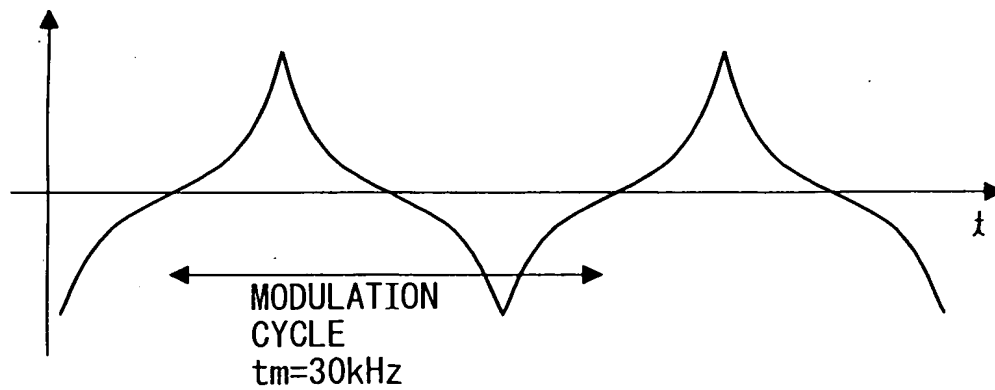


FIG. 4

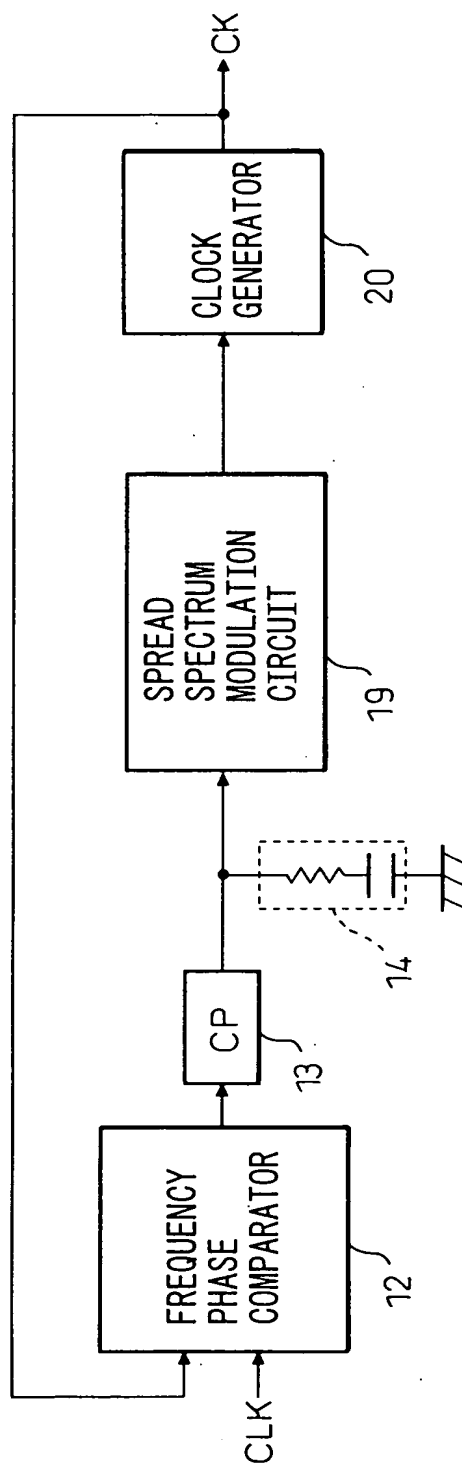


FIG. 5A

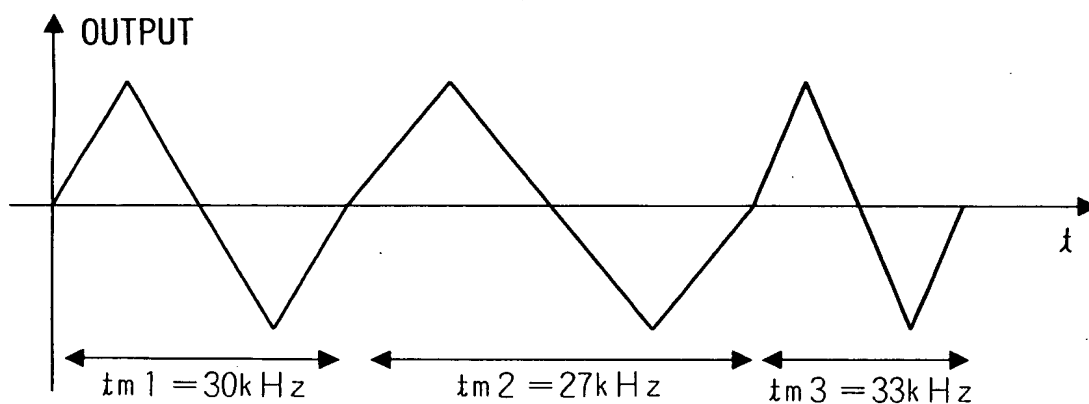
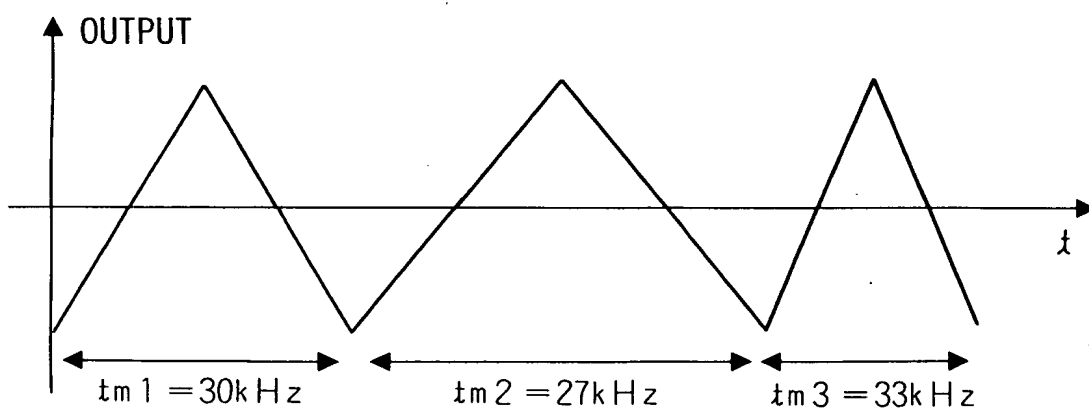


FIG. 5B



5/62

FIG. 6B

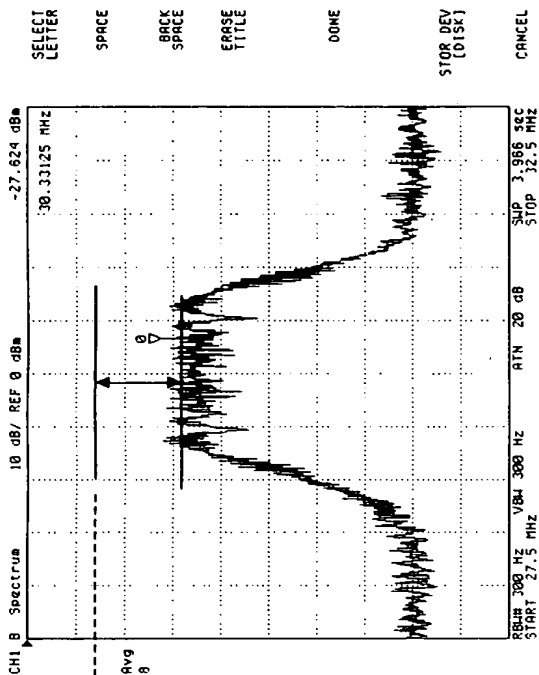
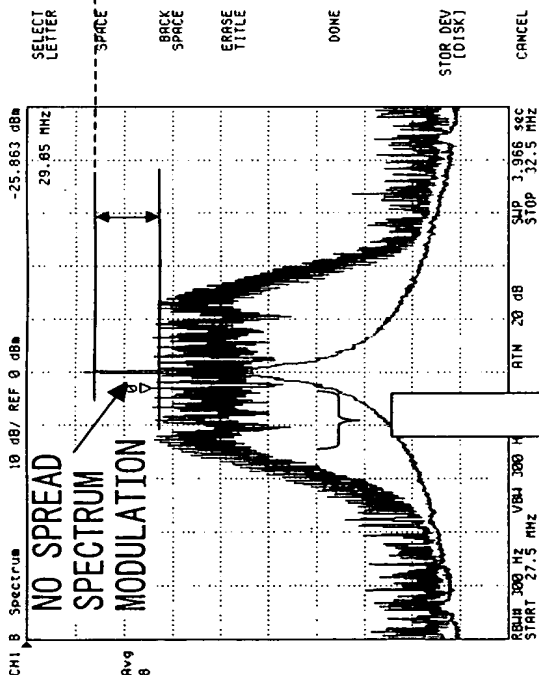


FIG. 6A



6/62

FIG. 7A

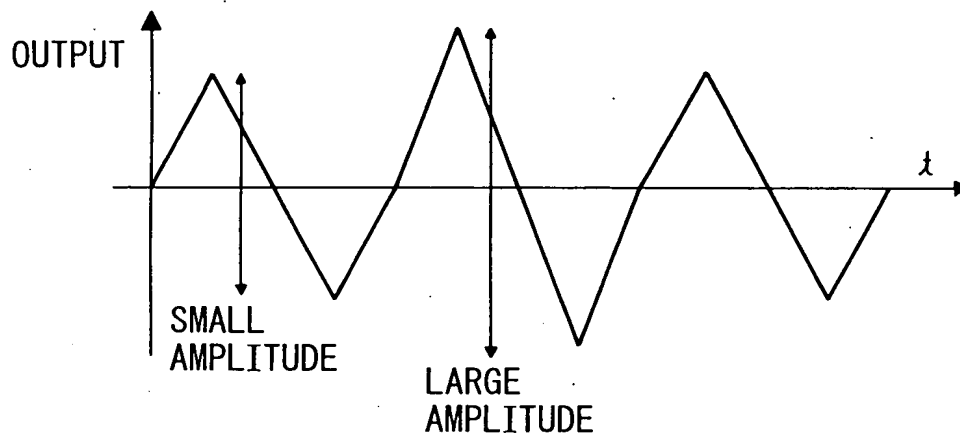


FIG. 7B

SPECTRUM WHEN A
SMALL AMPLITUDE
IS APPLIED

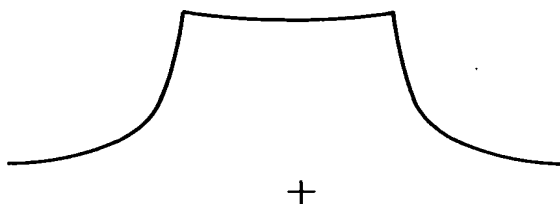


FIG. 7C

SPECTRUM WHEN A
LARGE AMPLITUDE
IS APPLIED

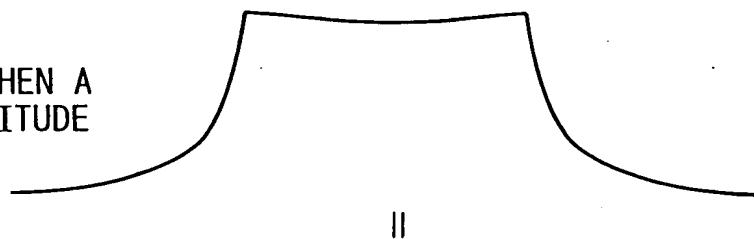


FIG. 7D

SYNTHESIZED
SPECTRUM



FIG.8

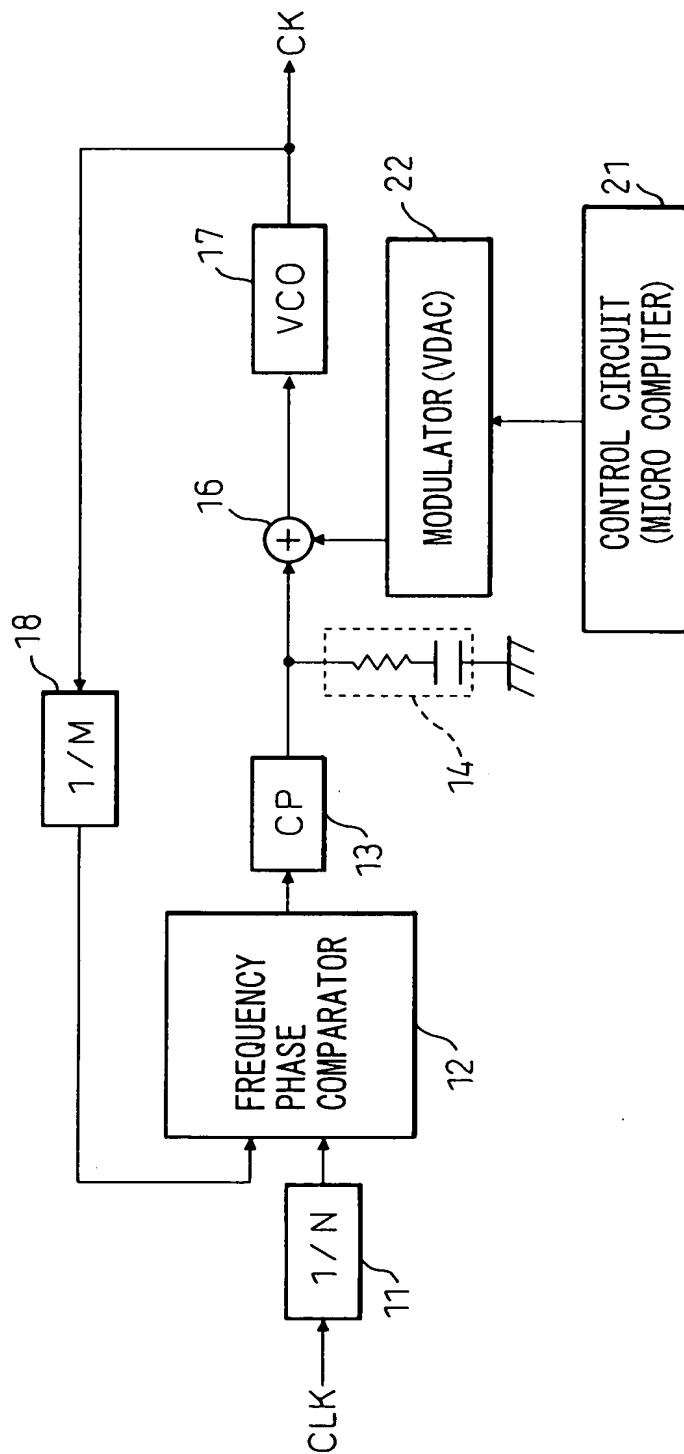


FIG. 9

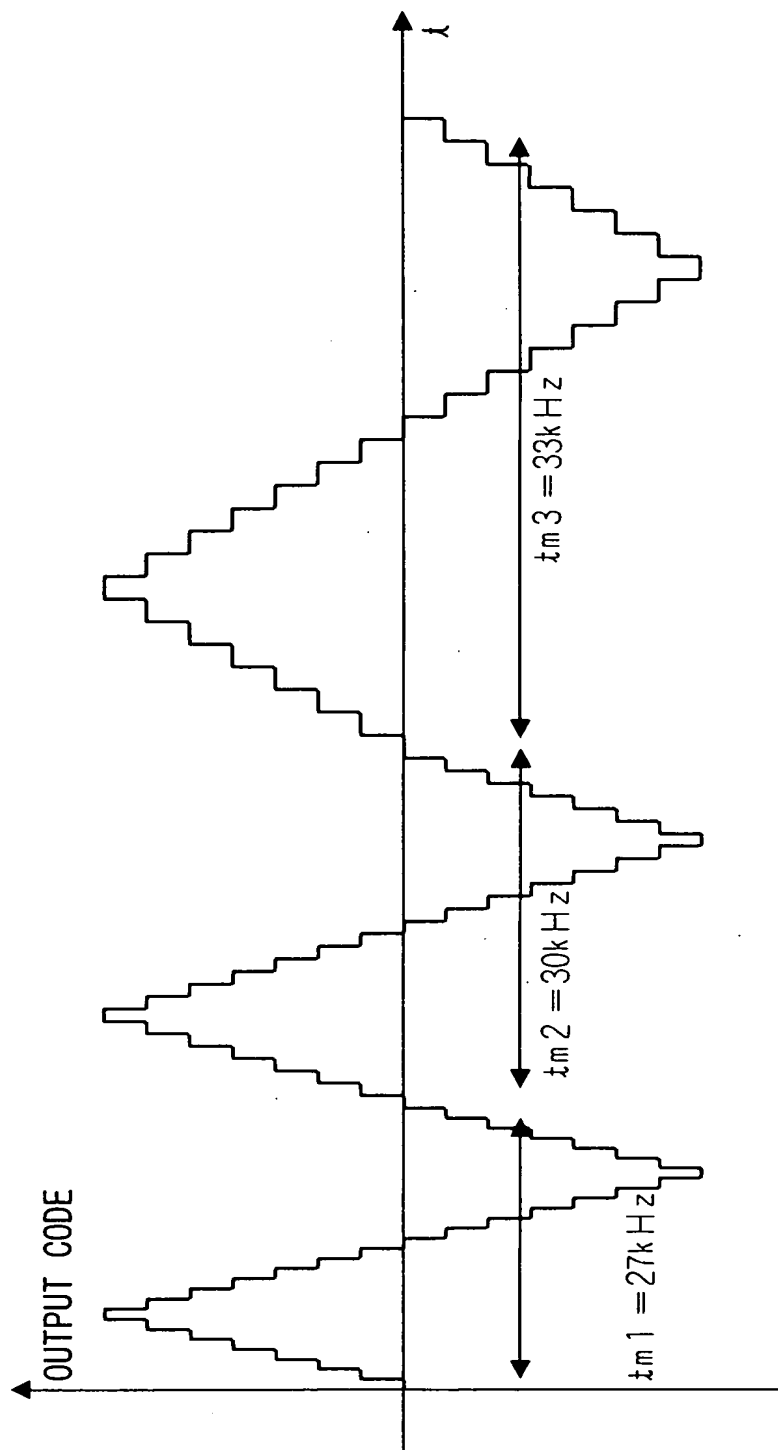
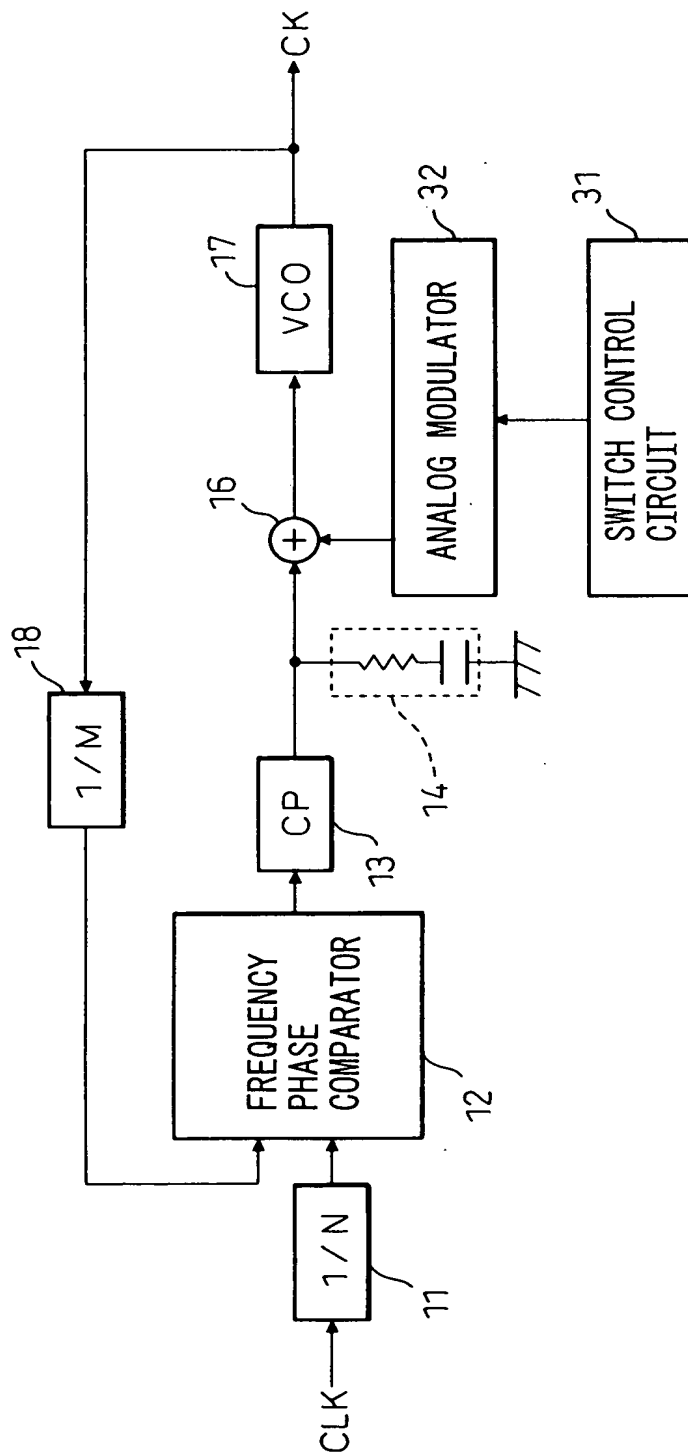


FIG.10



10/62

FIG.11

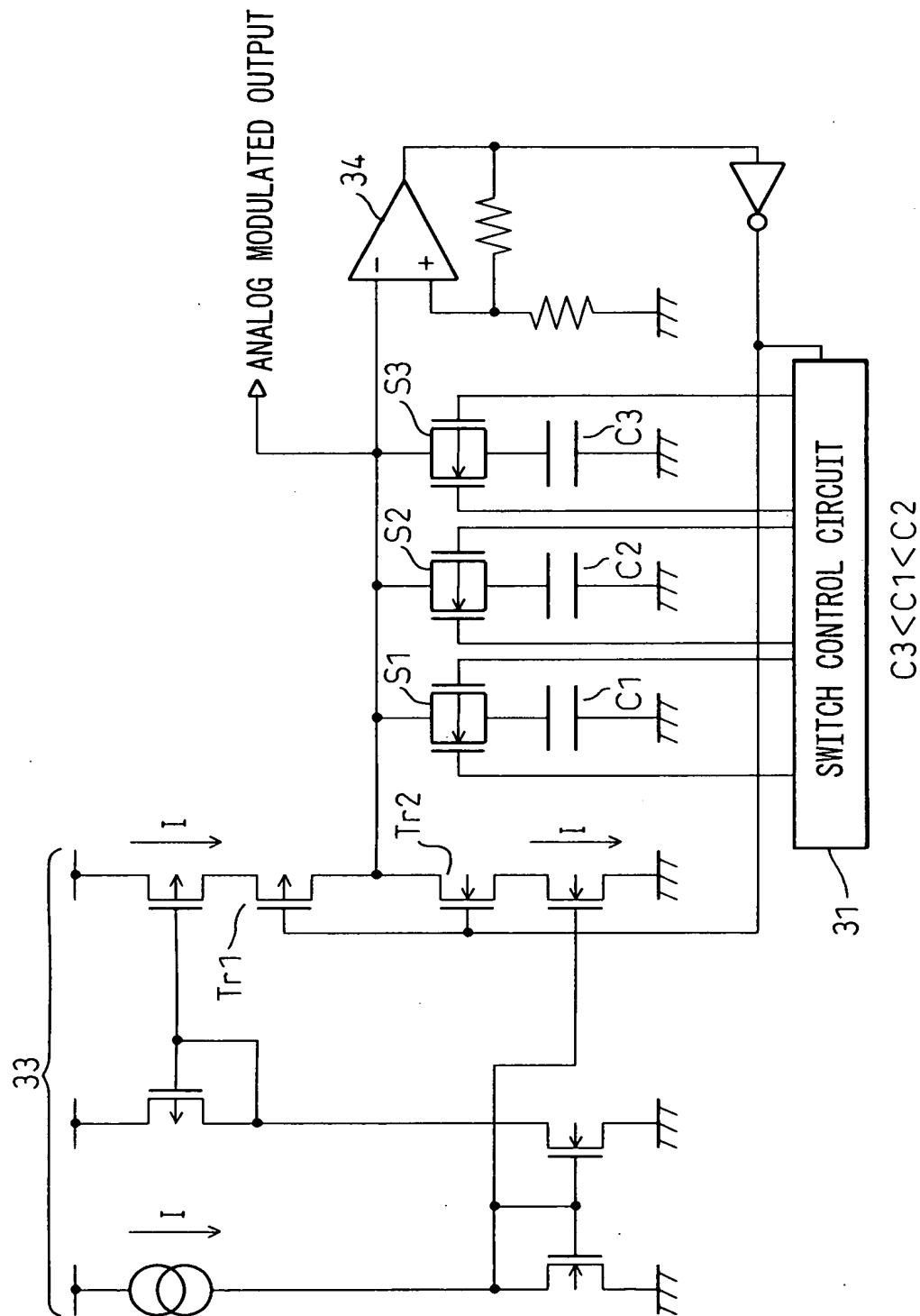
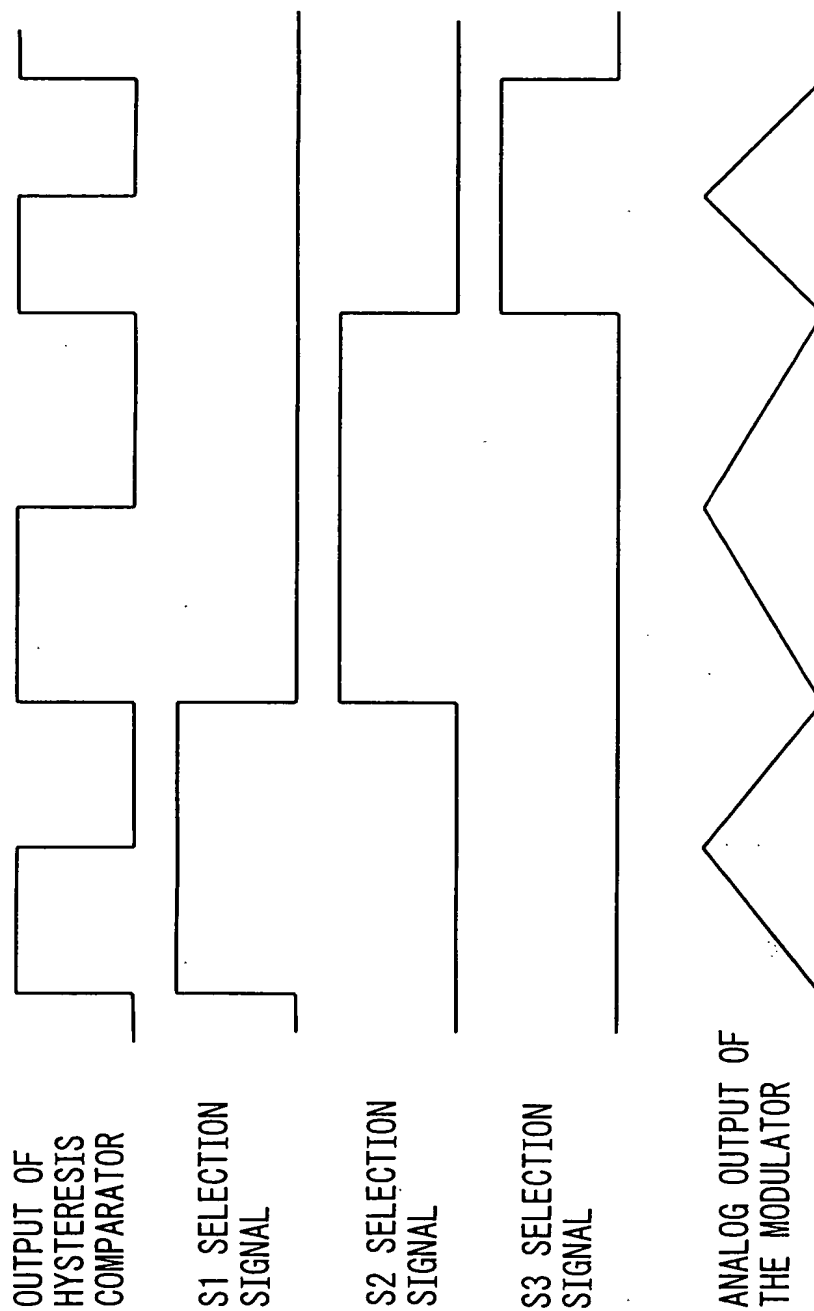


FIG.12



12/62

FIG.13

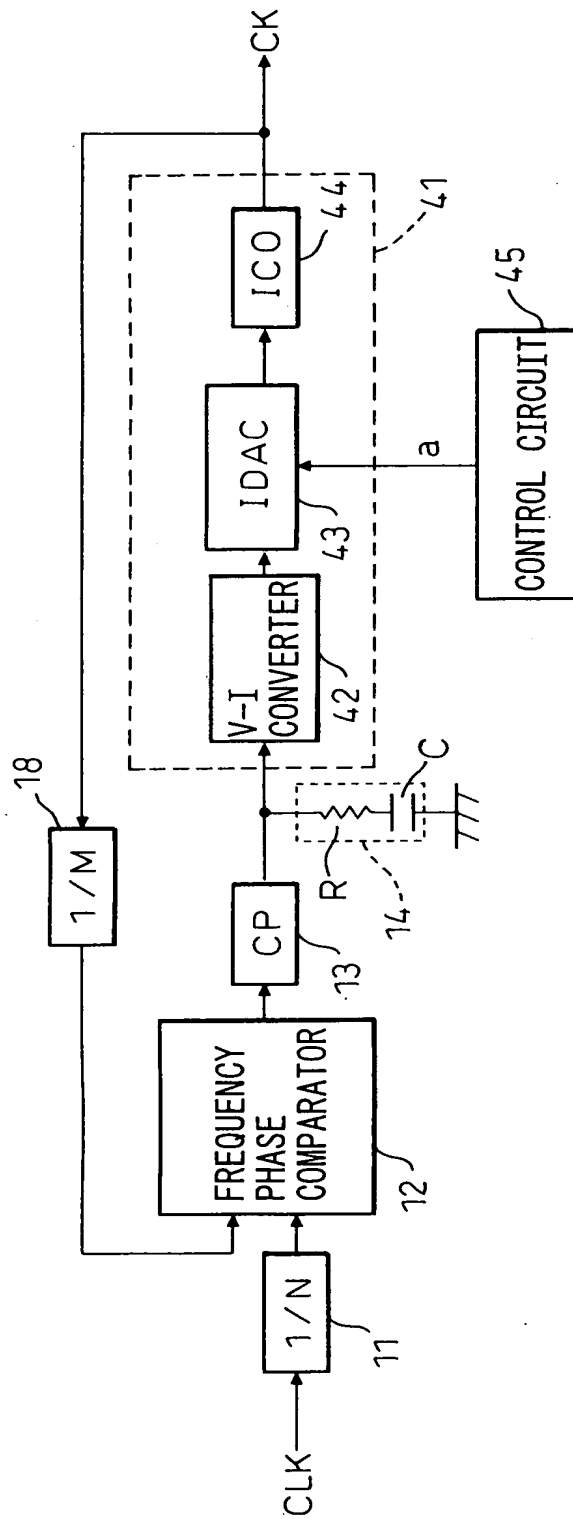


FIG. 14

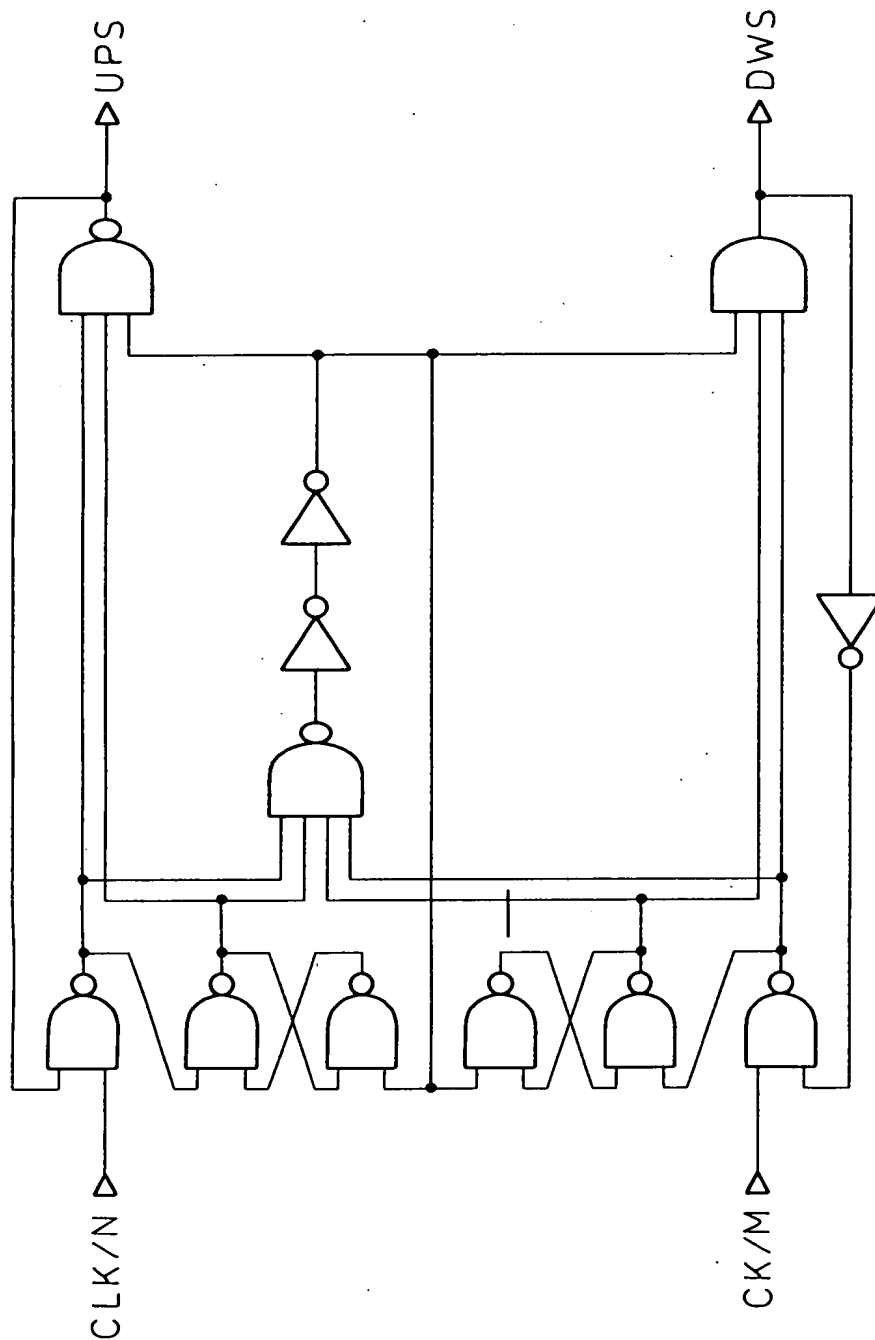


FIG.15

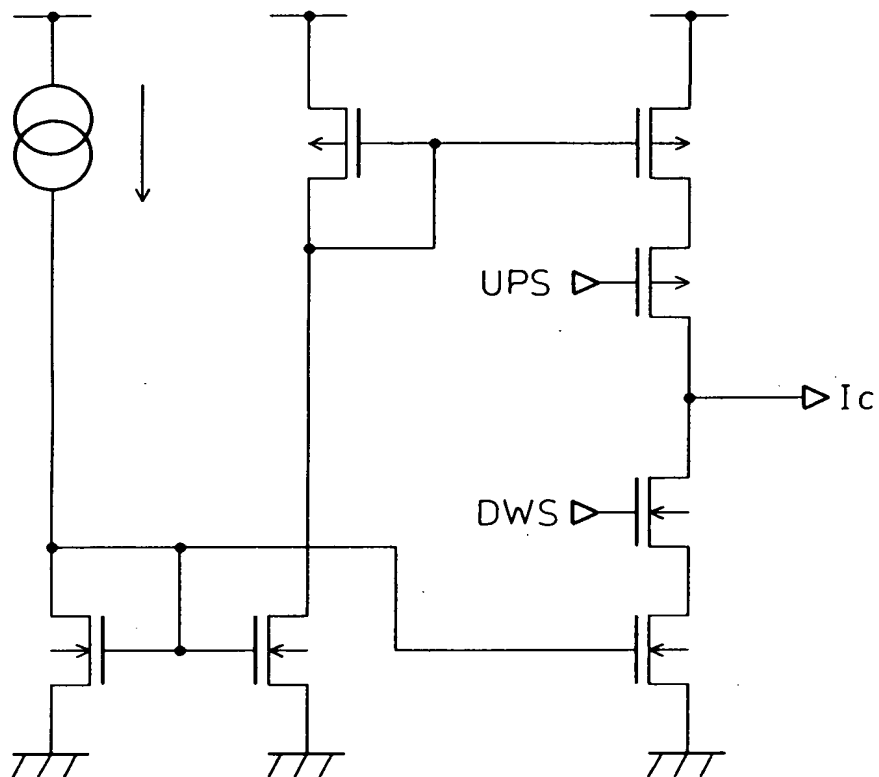


FIG.16

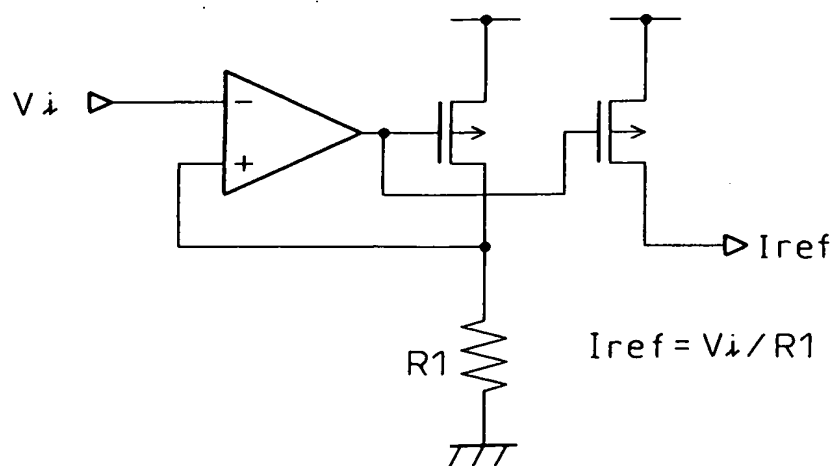


FIG.17

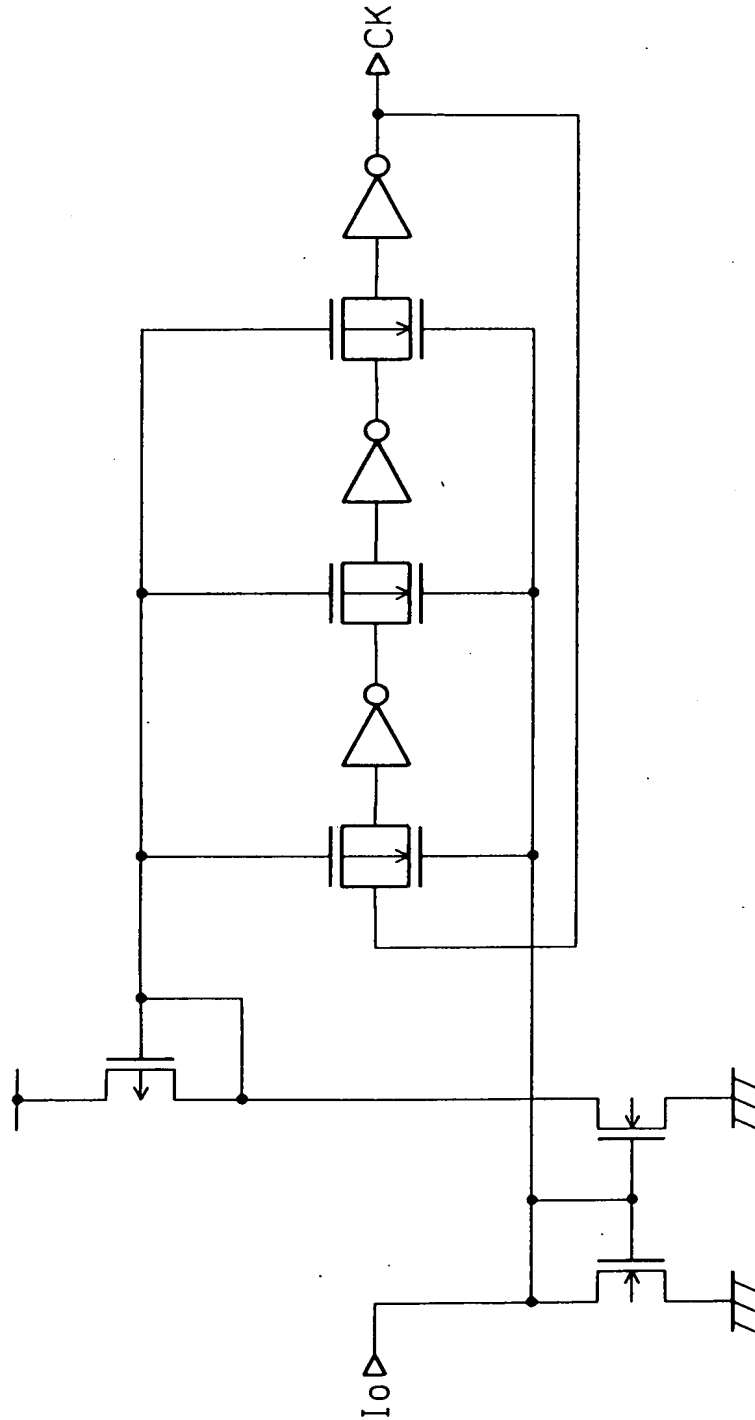


FIG.18

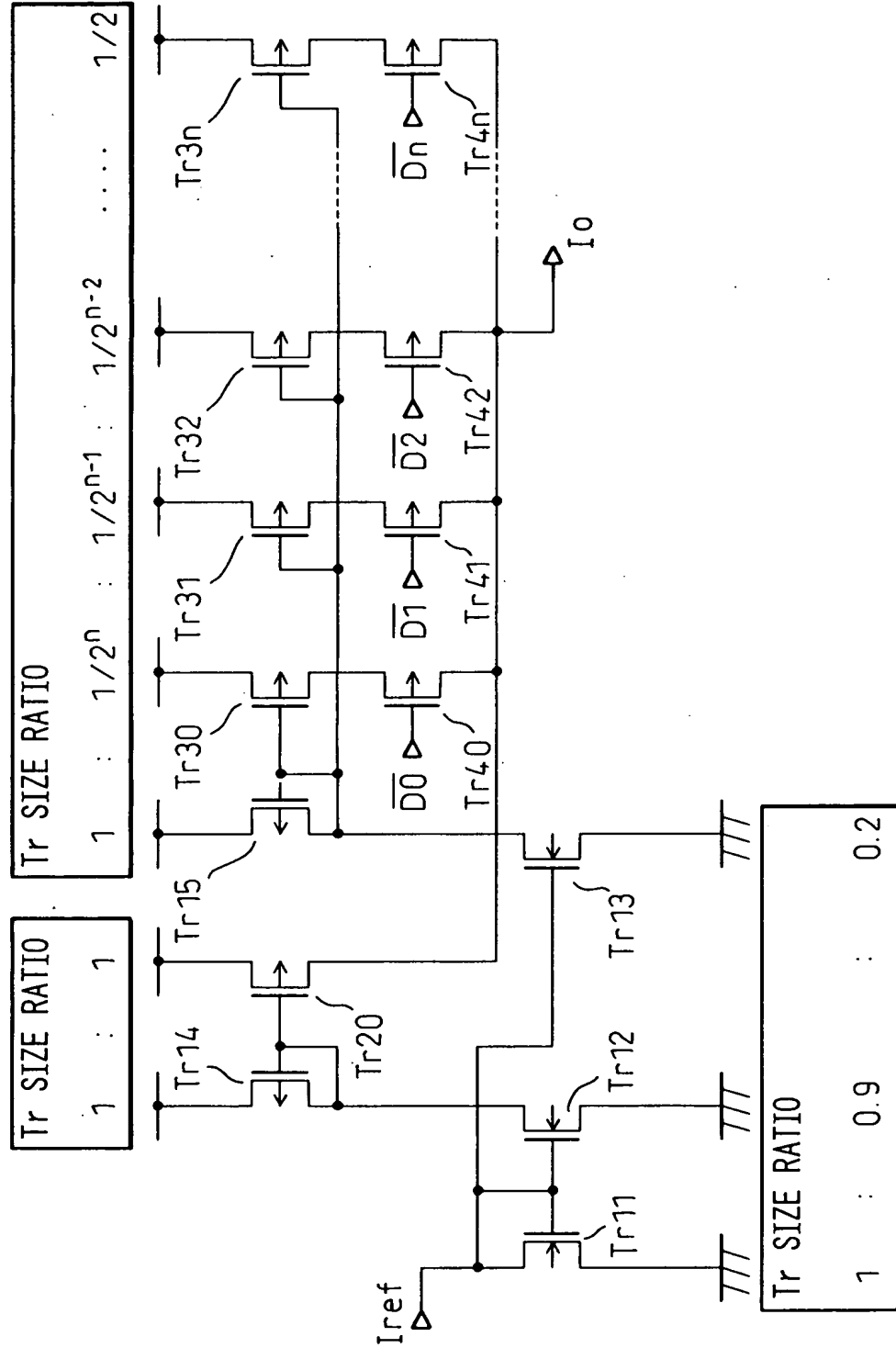


FIG.19

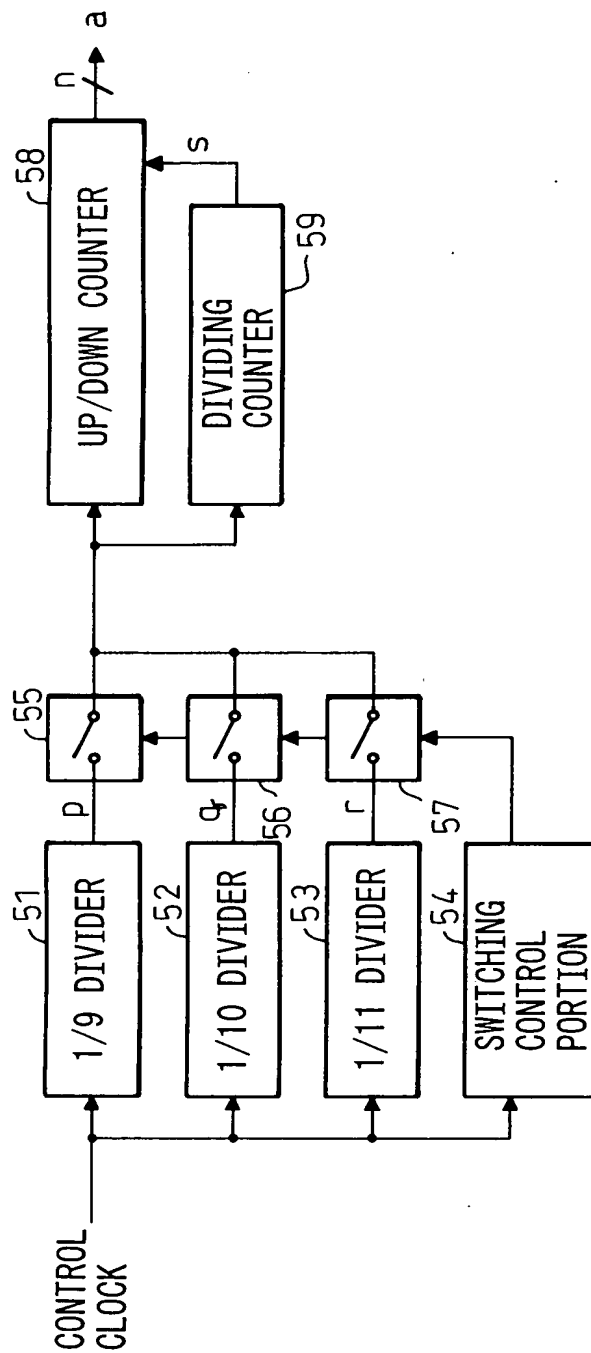


FIG. 20

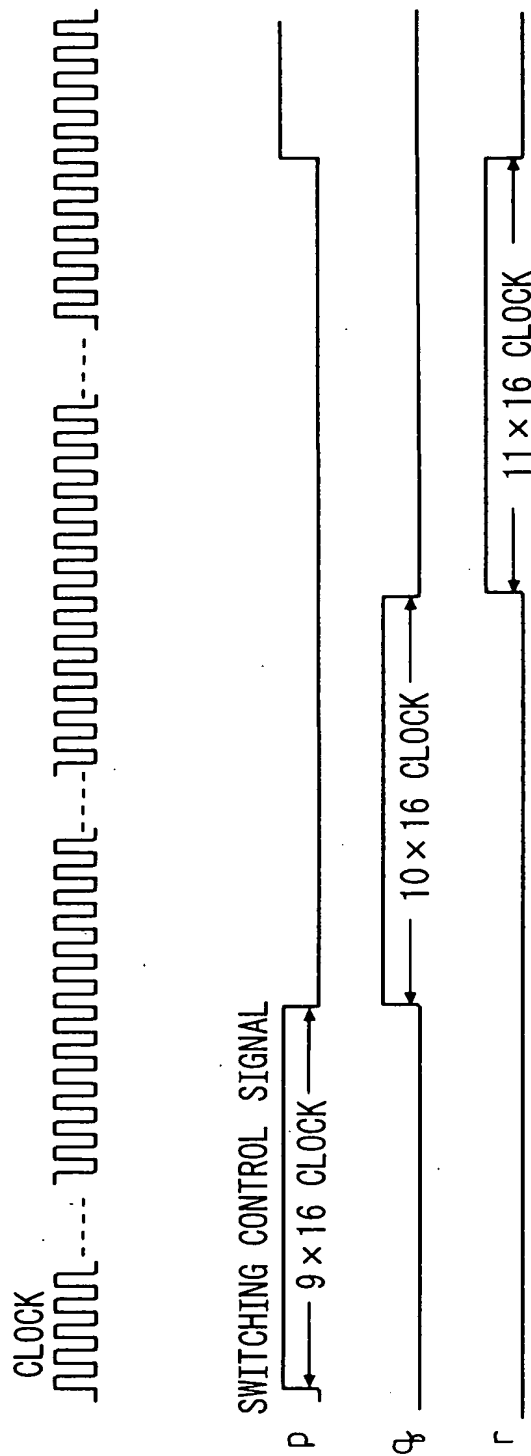


FIG. 21

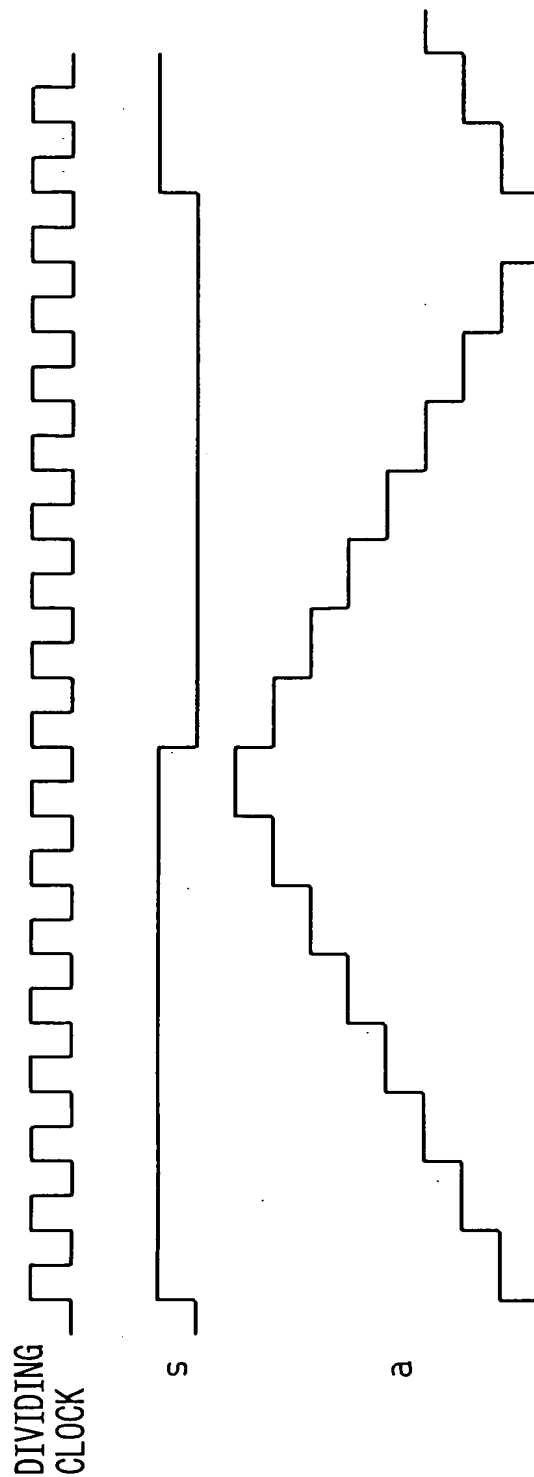


FIG. 22

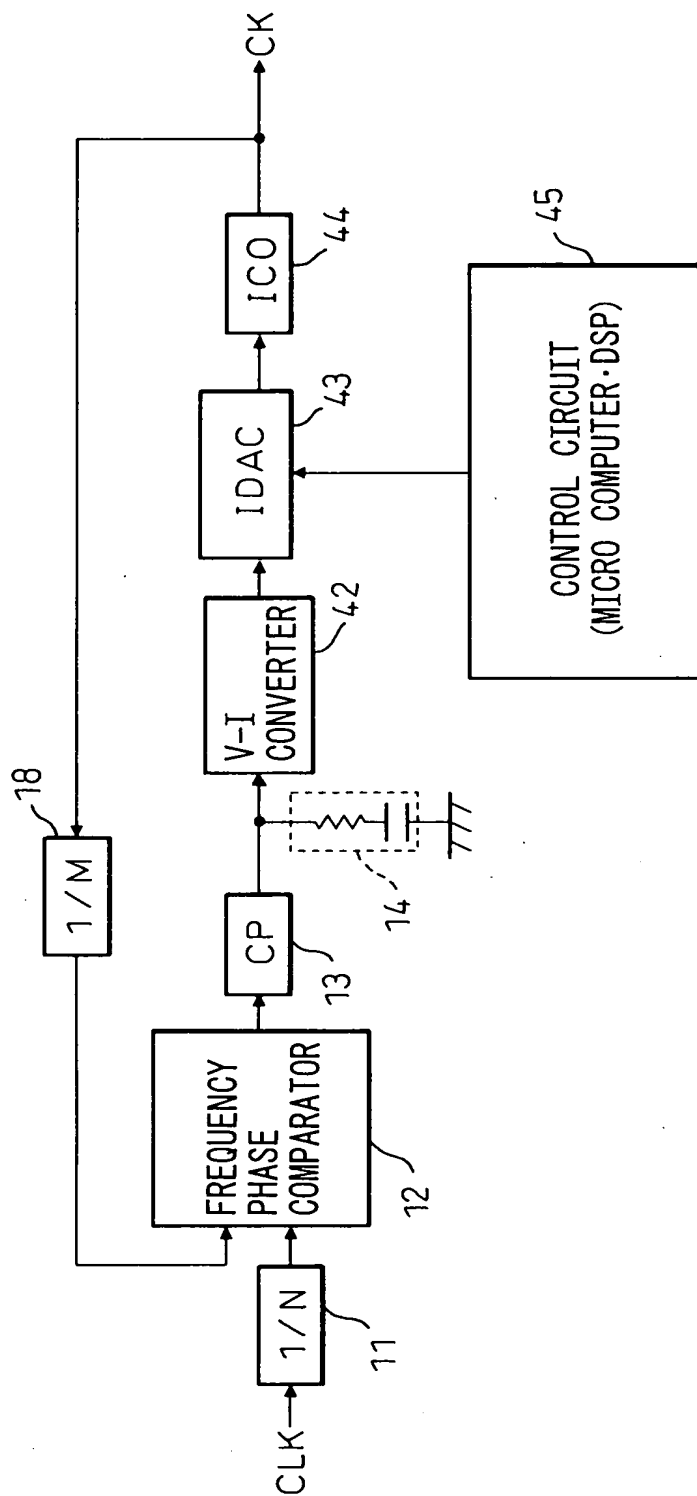


FIG. 23

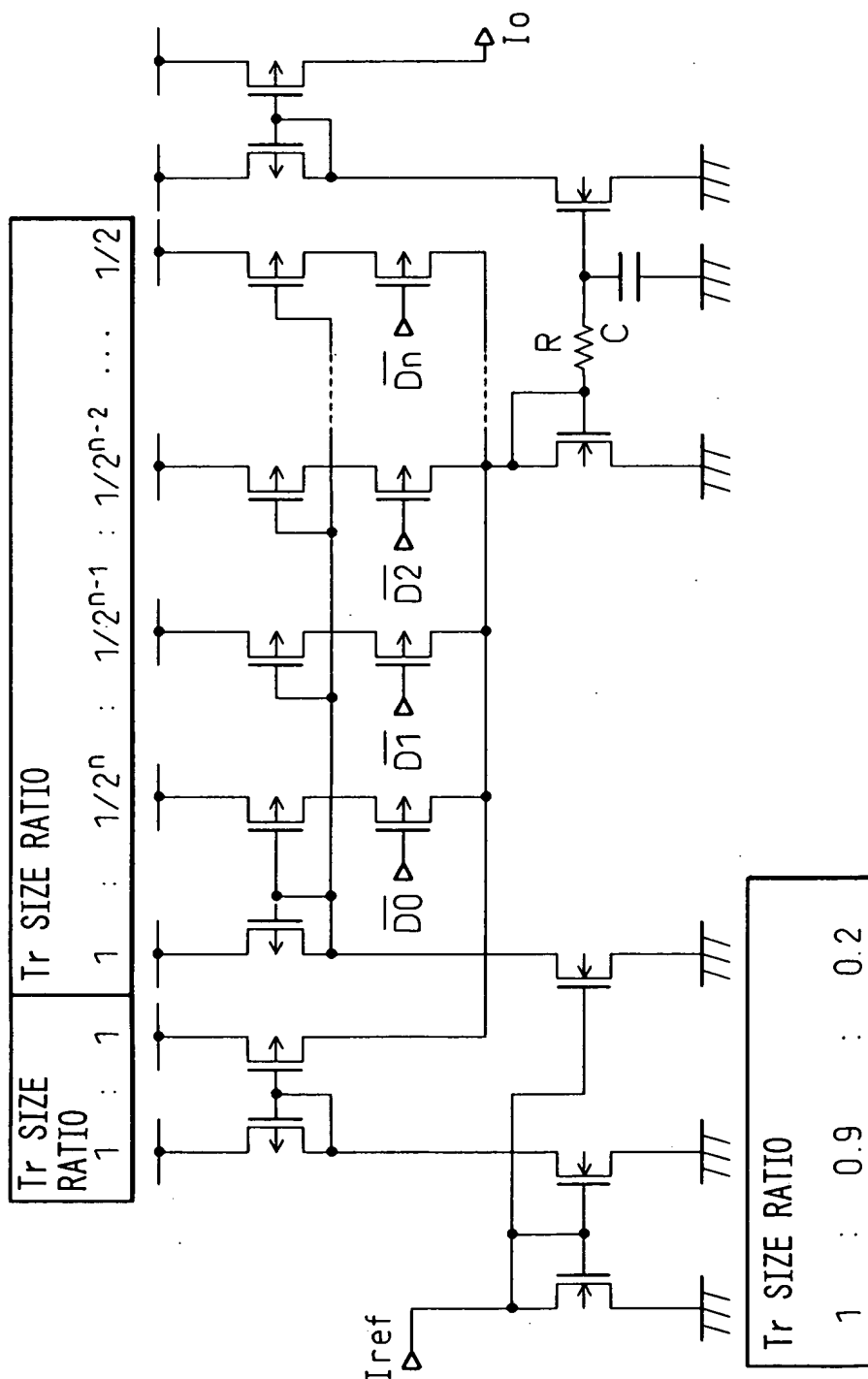


FIG. 24

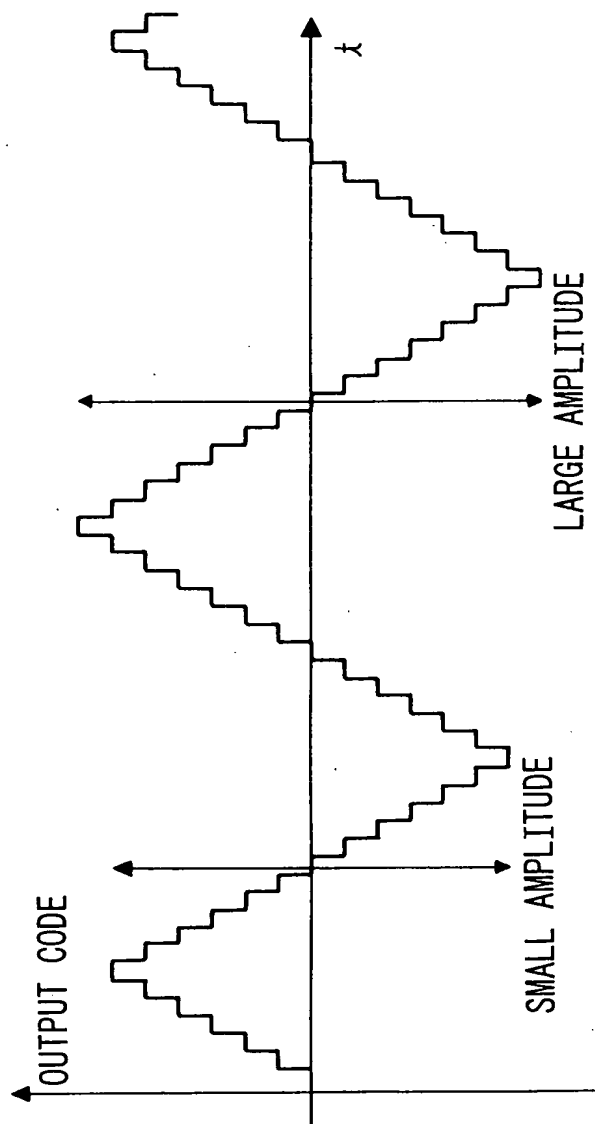


FIG. 25A

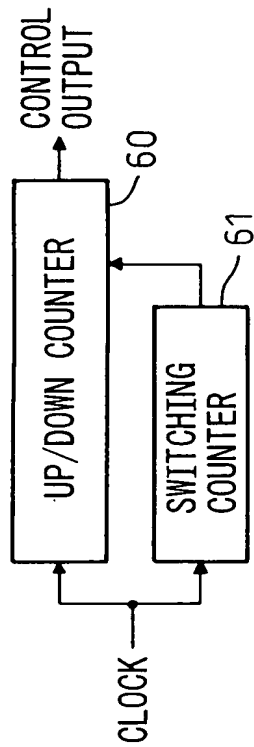
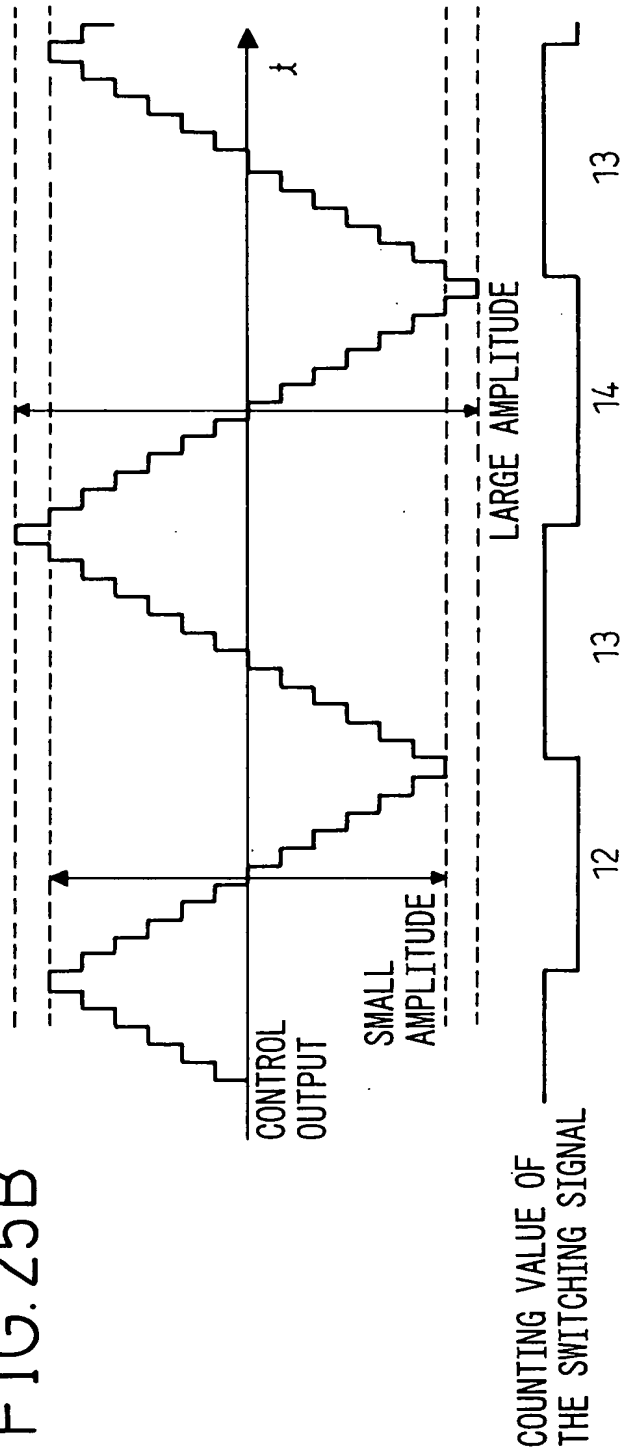


FIG. 25B



The diagram shows a modulator circuit 36. It features a current source I connected to a differential pair of transistors. The gates of these transistors are controlled by a switch control circuit 35. The circuit also includes a current mirror and a load capacitor $C1$. The output is labeled MODULATOR OUTPUT.

FIG. 27

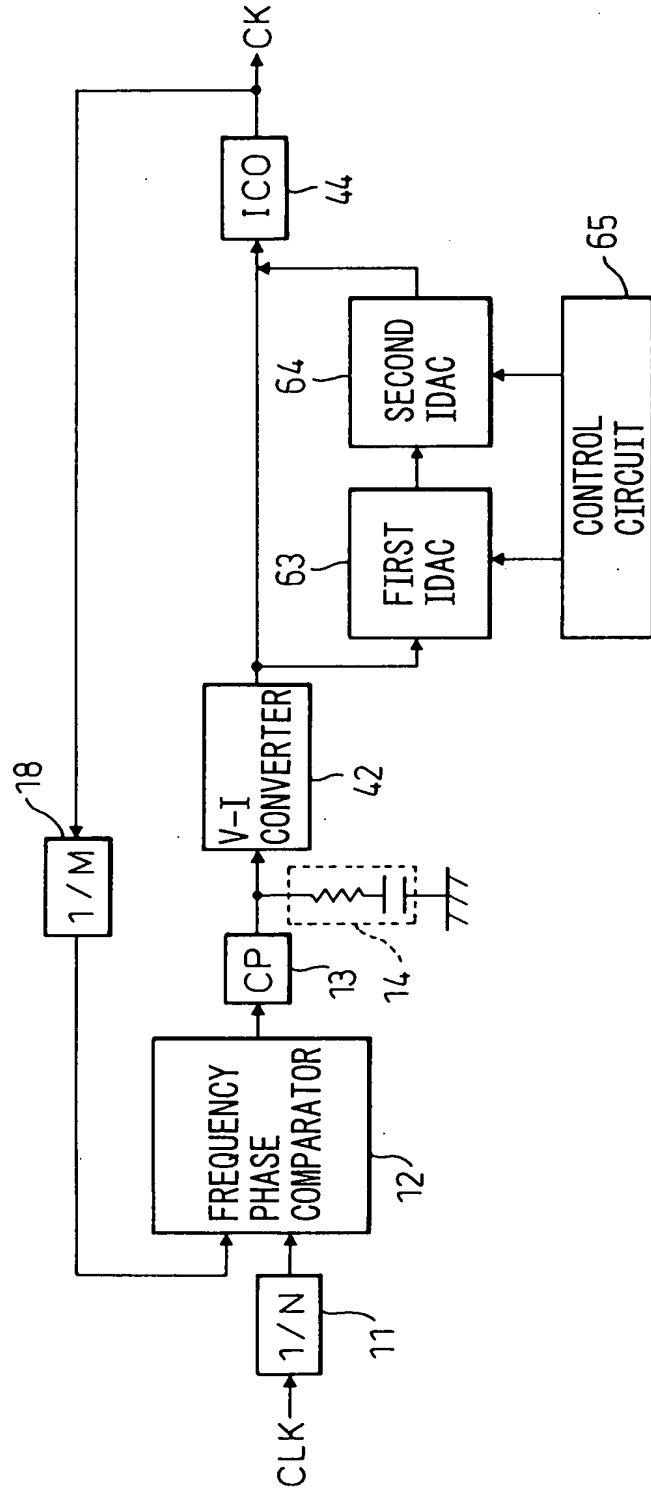


FIG. 28

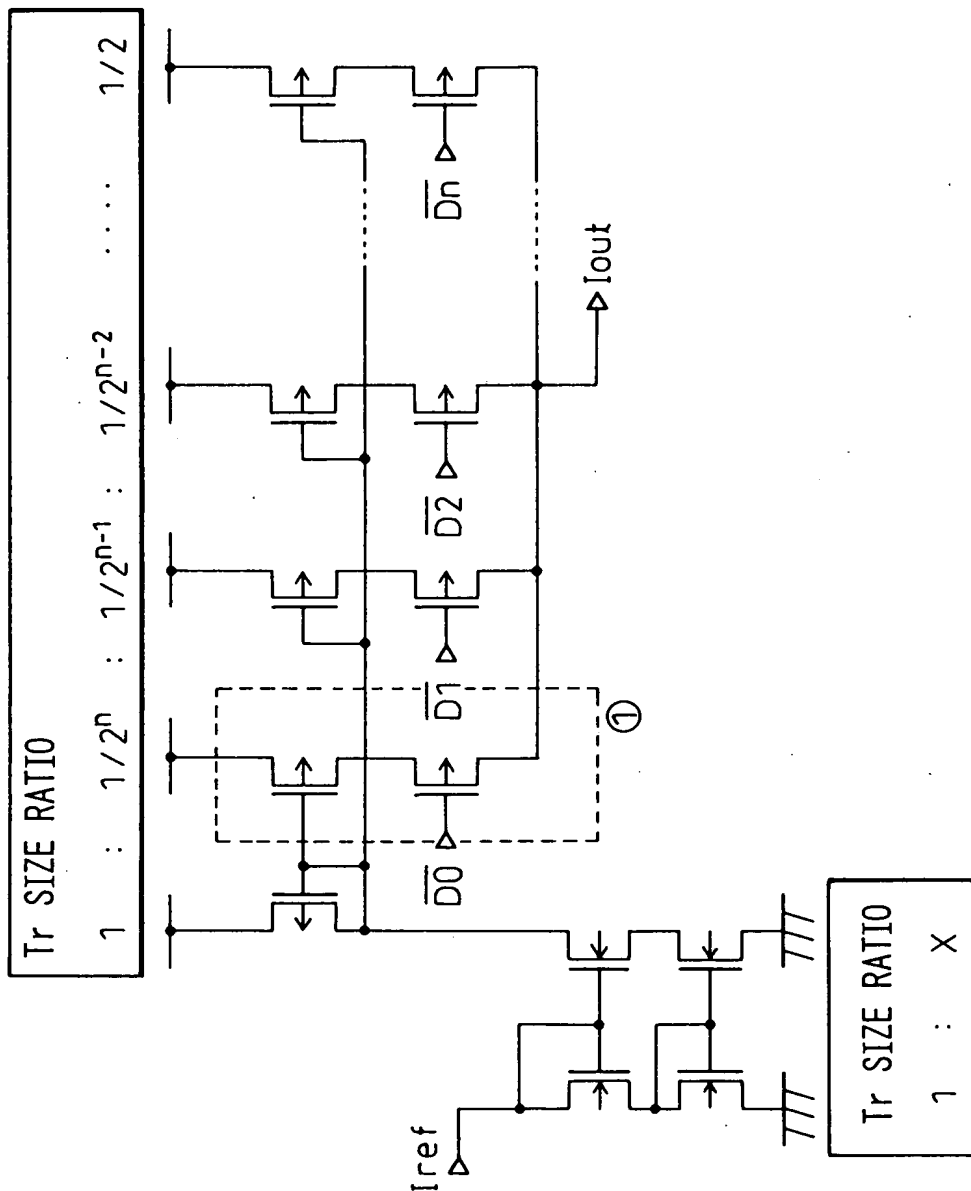


FIG. 29

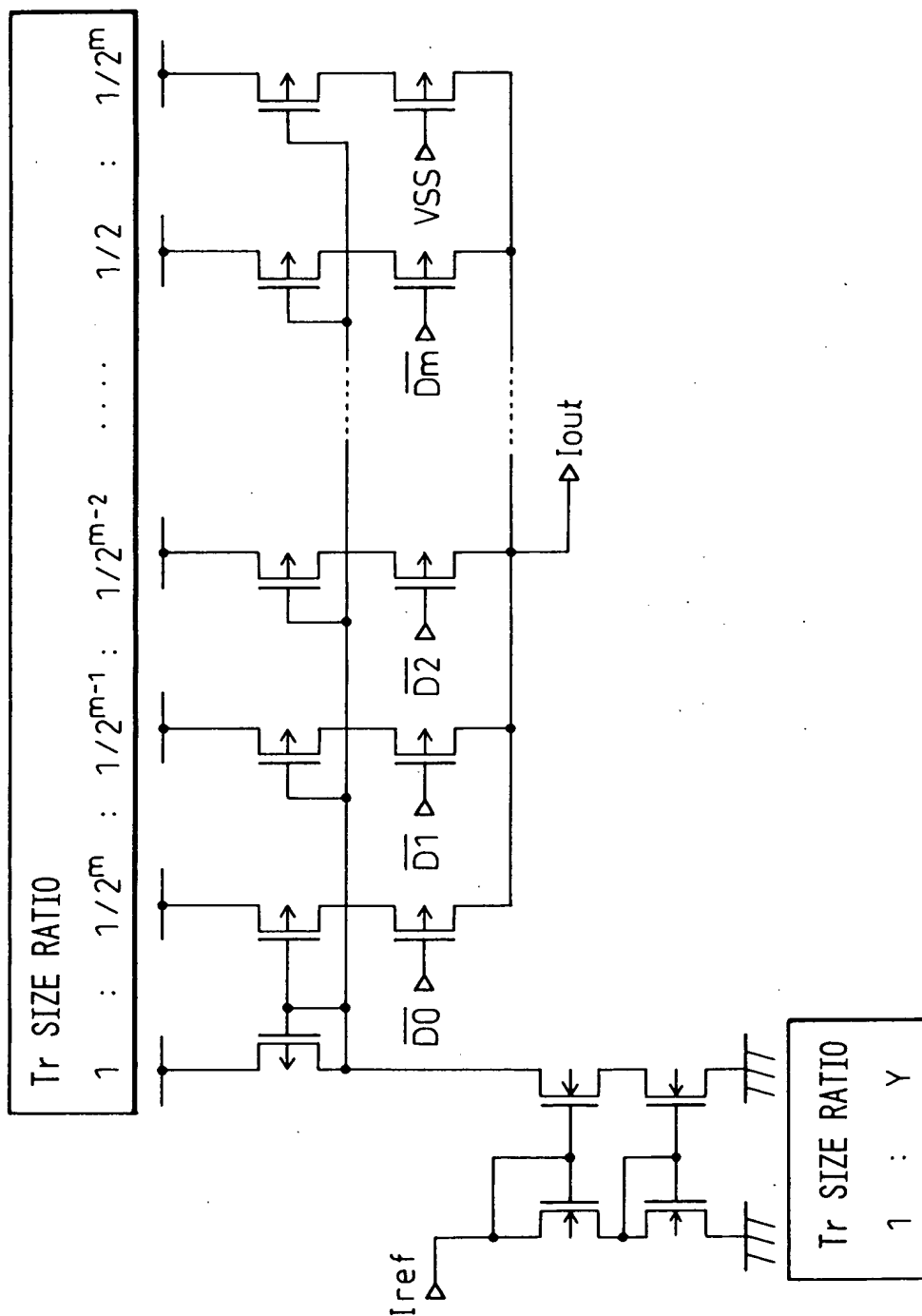


FIG.30A

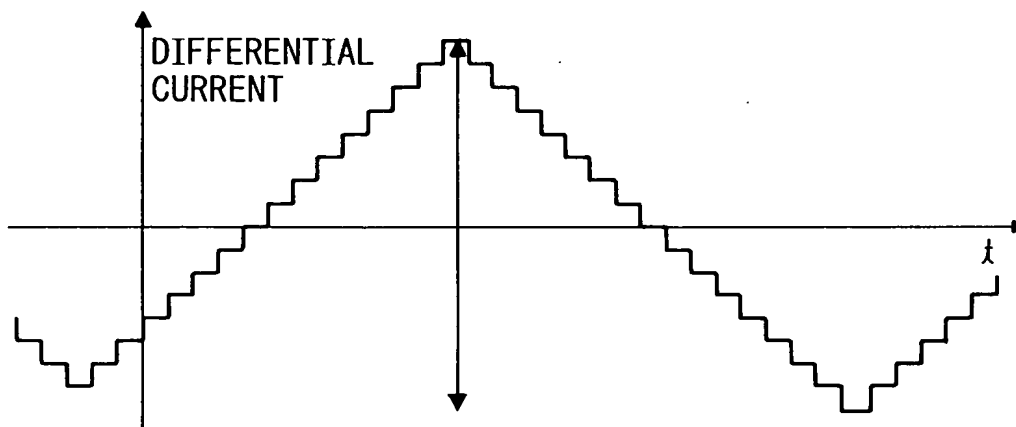


FIG.30B

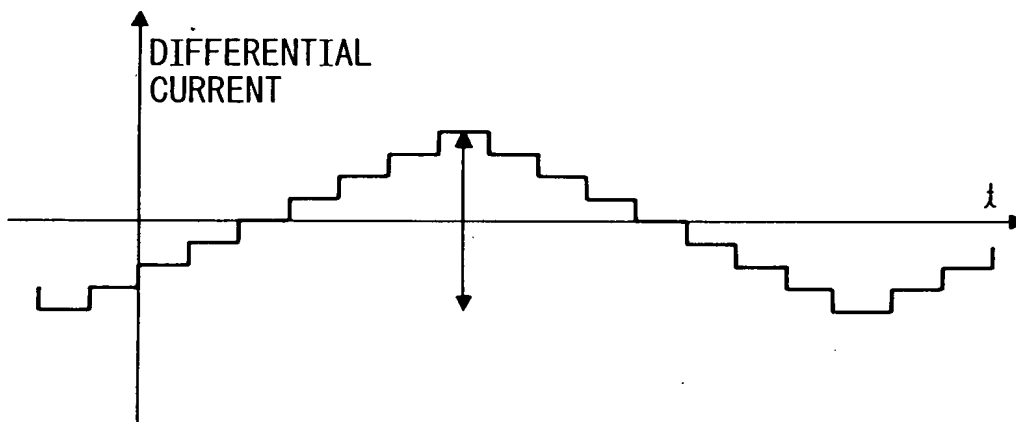


FIG.30C

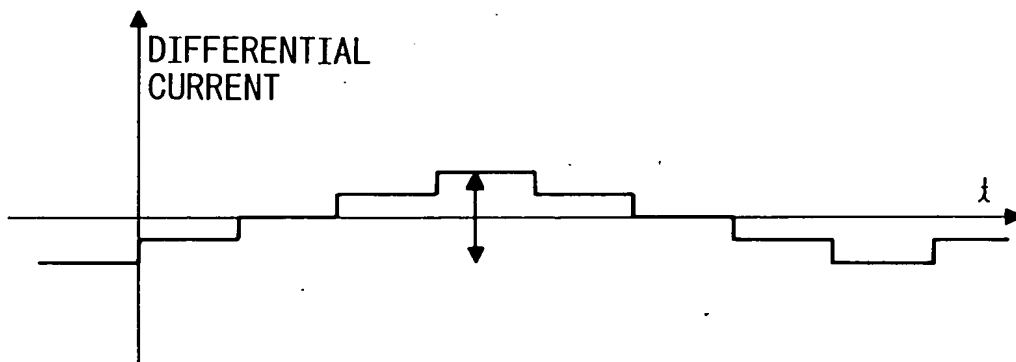


FIG. 31A

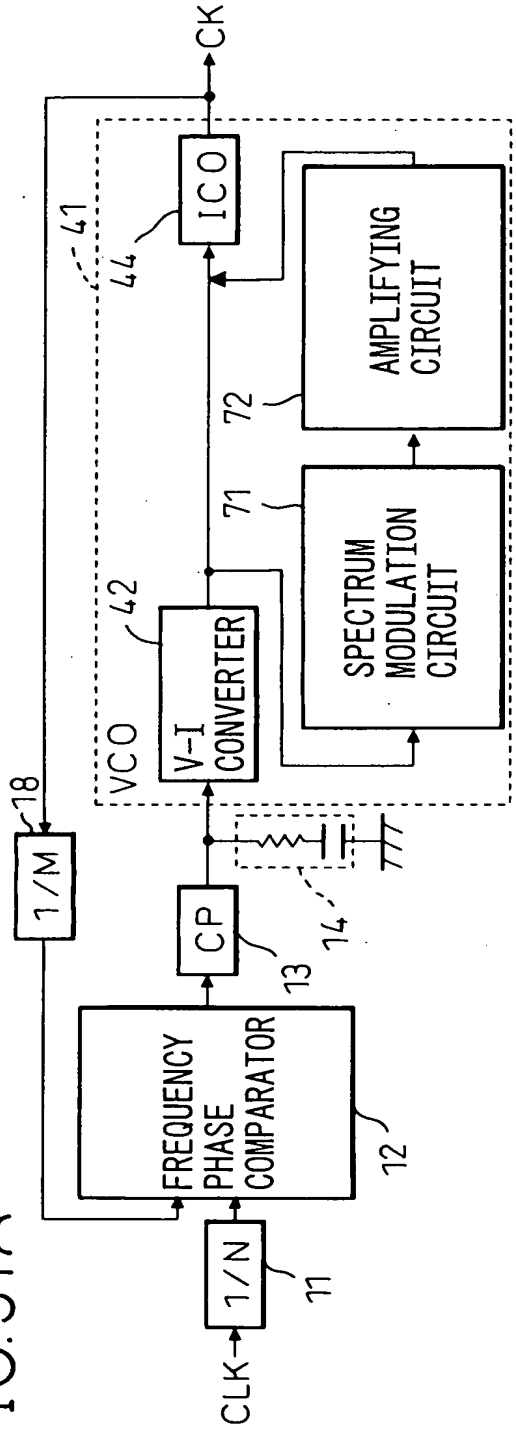


FIG. 31B

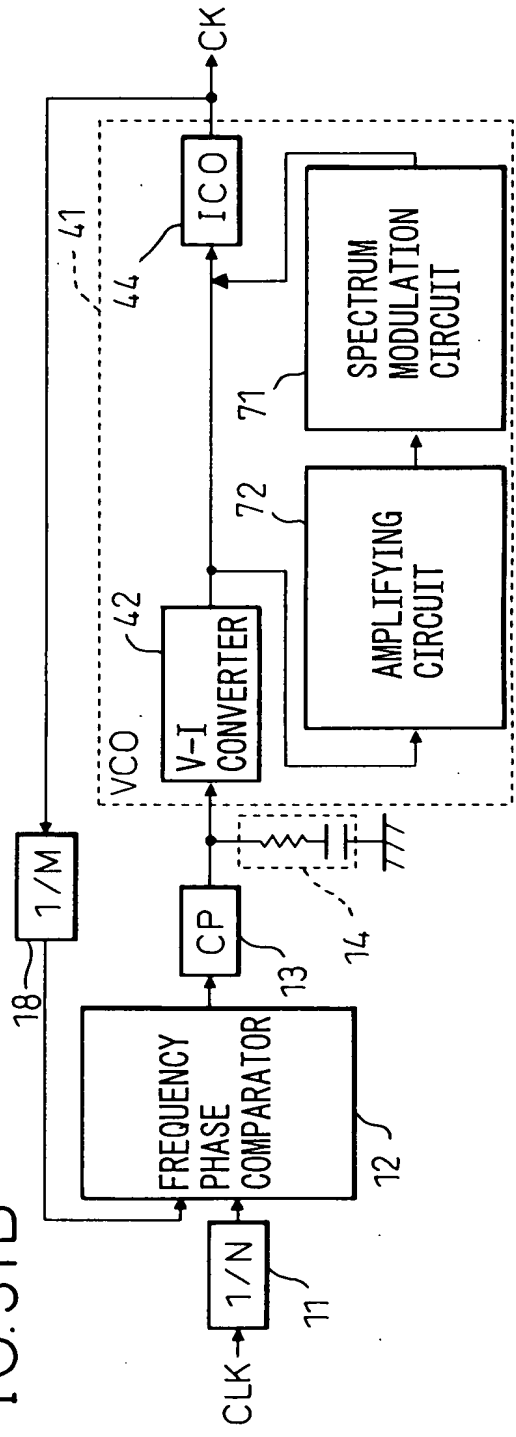


FIG. 32A

AMPLITUDE A

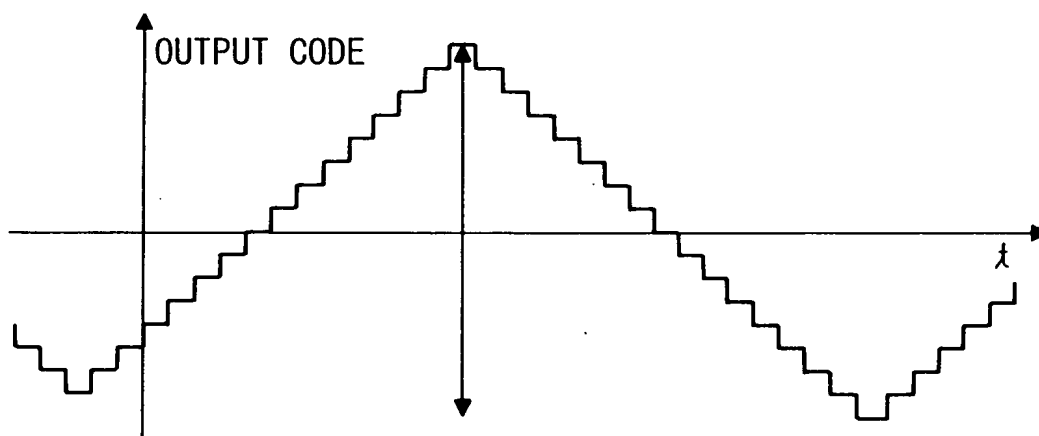


FIG. 32B

AMPLITUDE A k/m

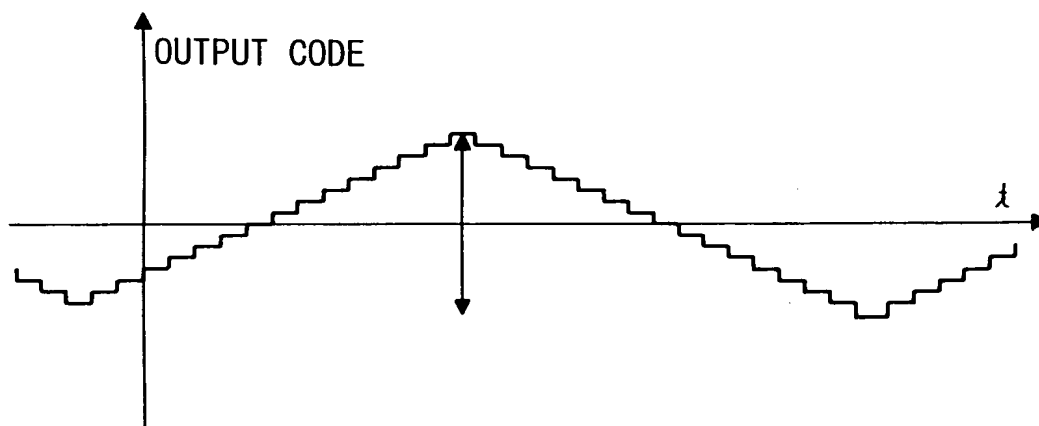


FIG. 33

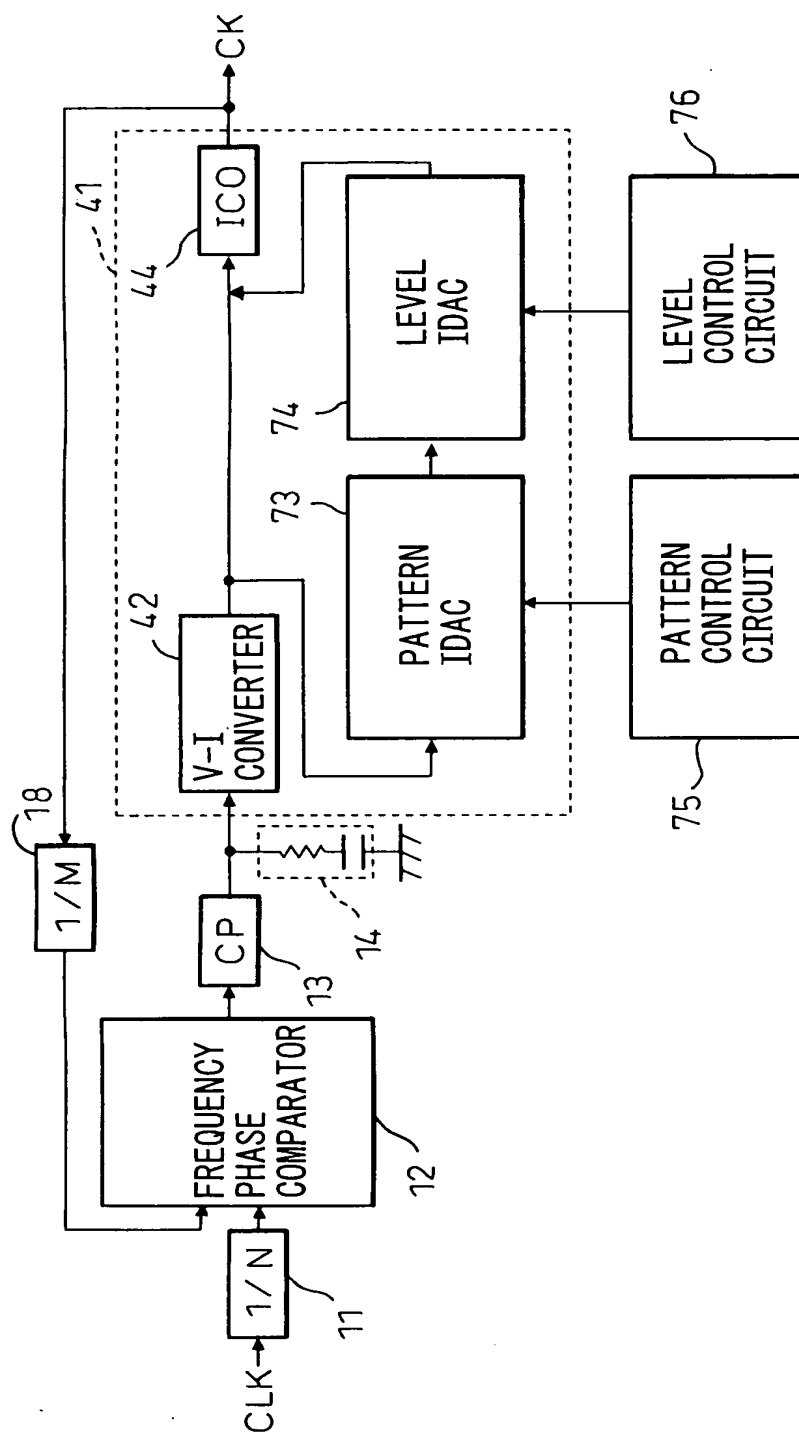


FIG. 34A

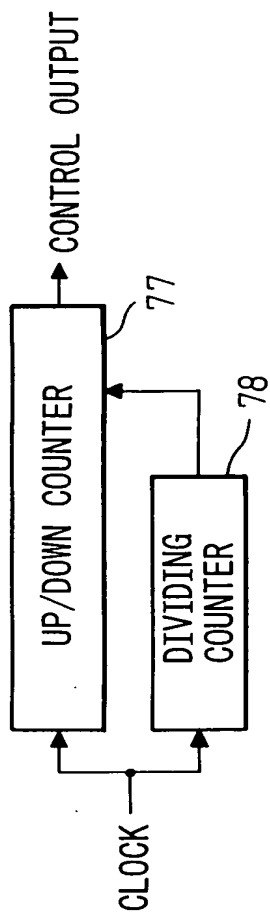


FIG. 34B

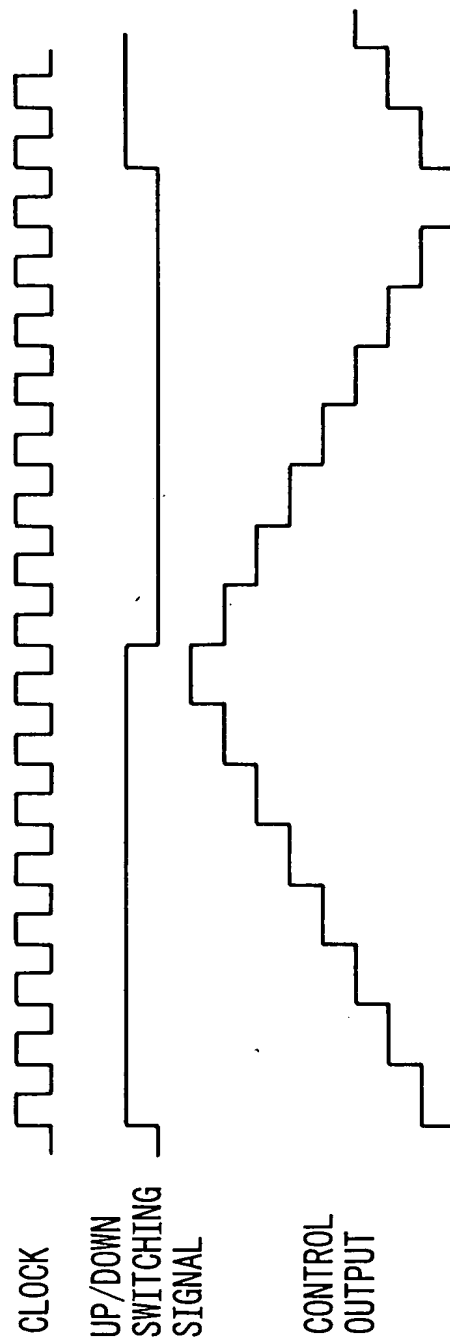


FIG.35

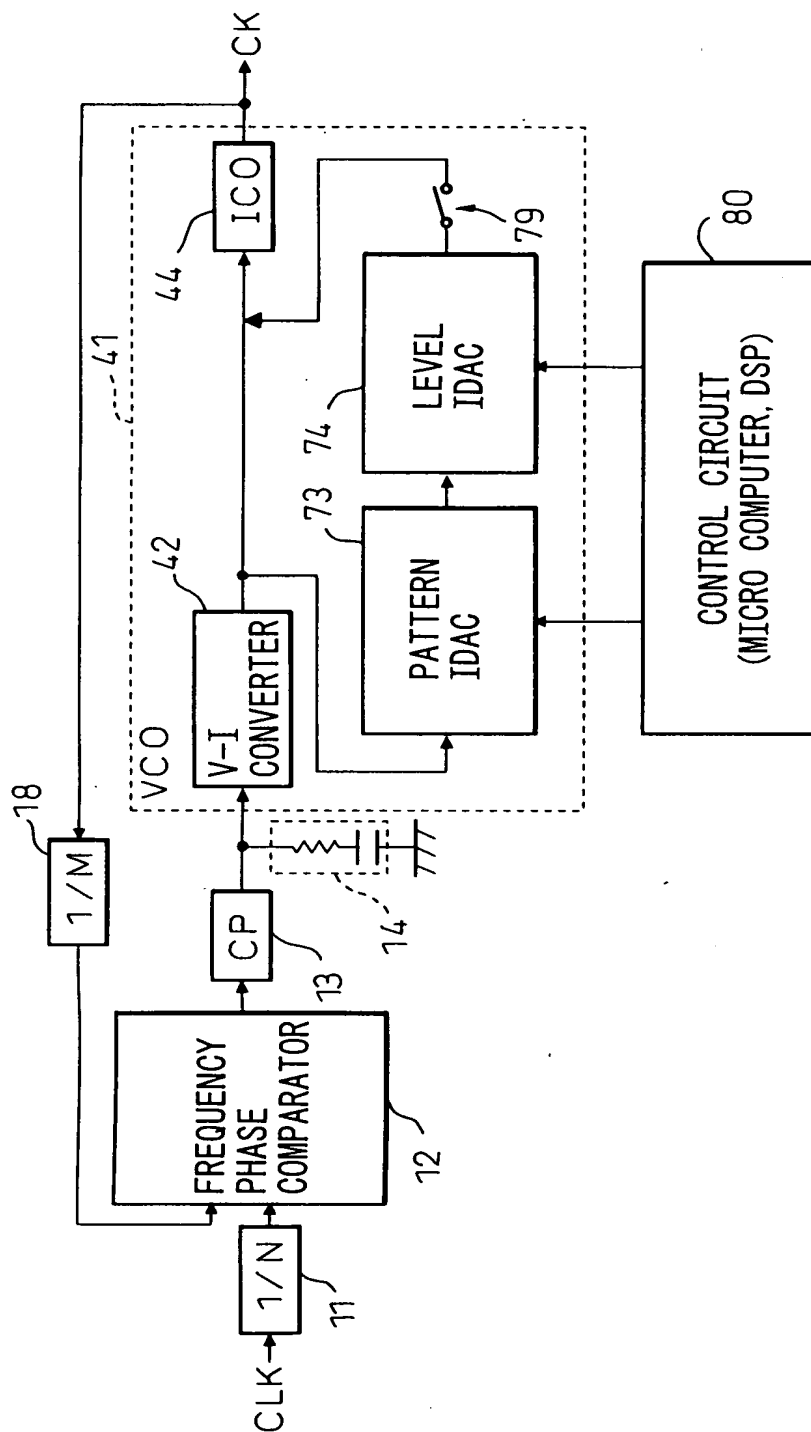


FIG. 36A

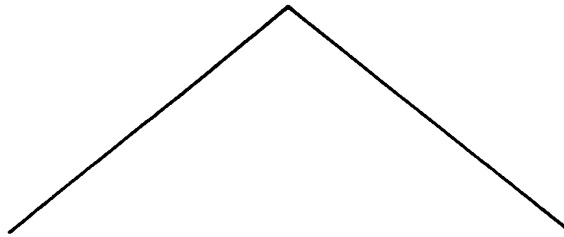


FIG. 36B

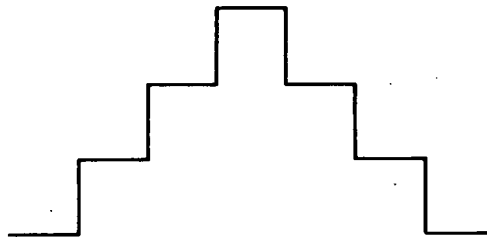


FIG. 36C

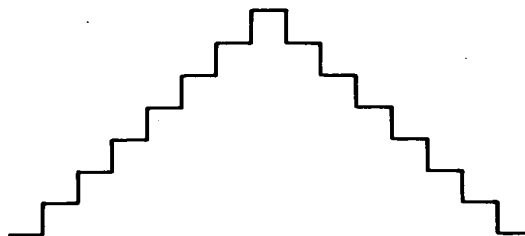


FIG. 37

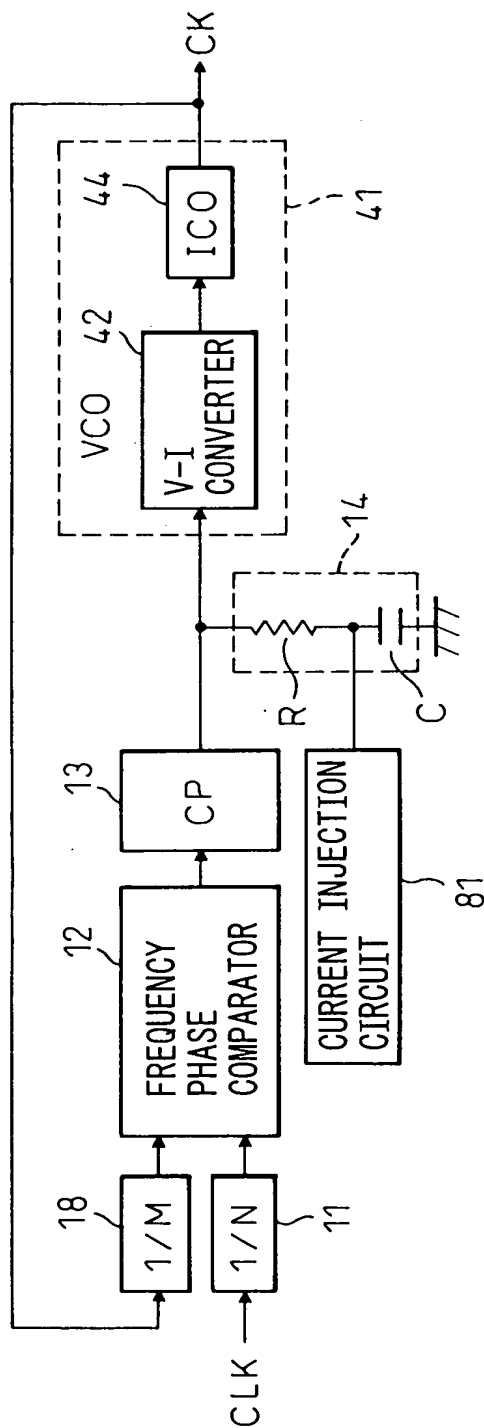


FIG. 38

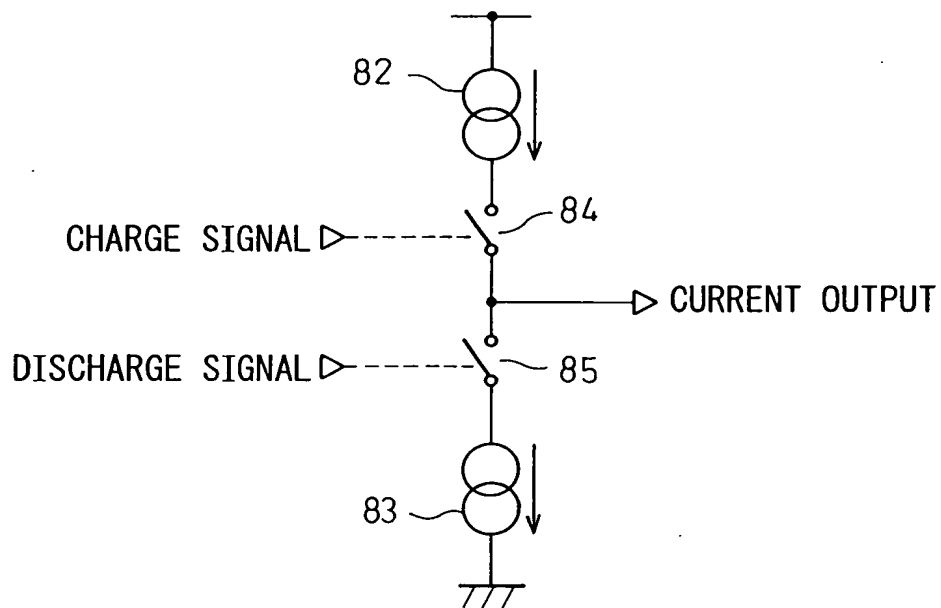


FIG. 39

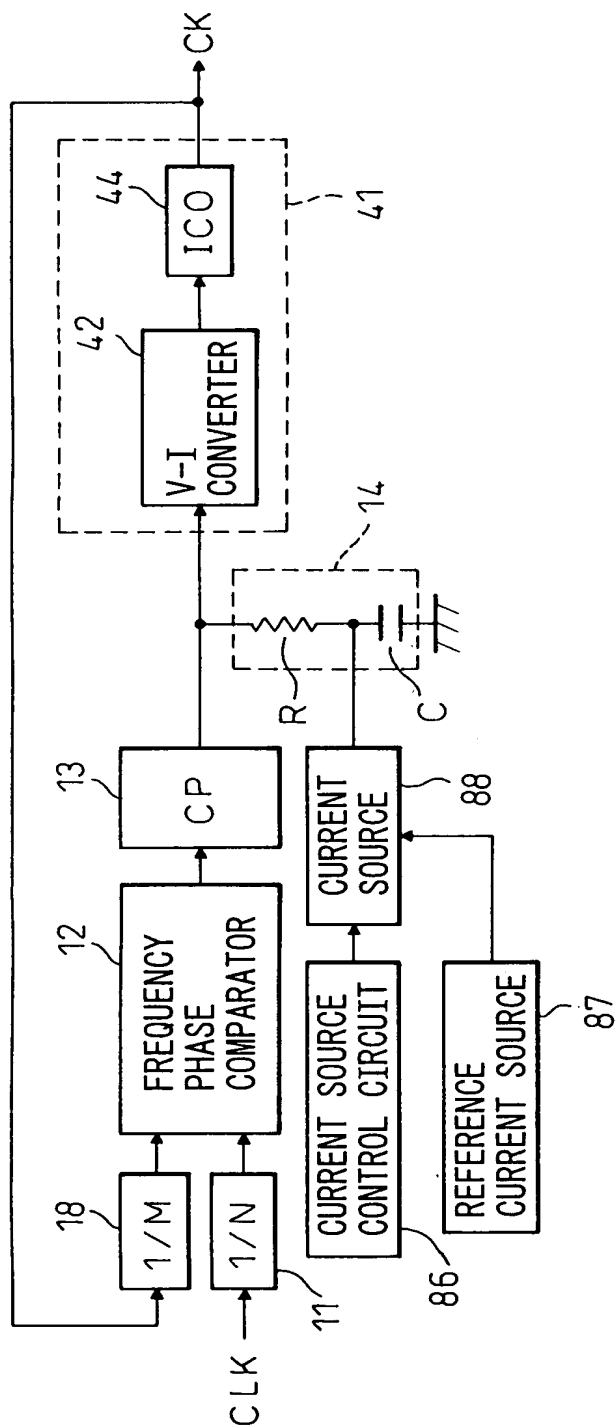


FIG. 40

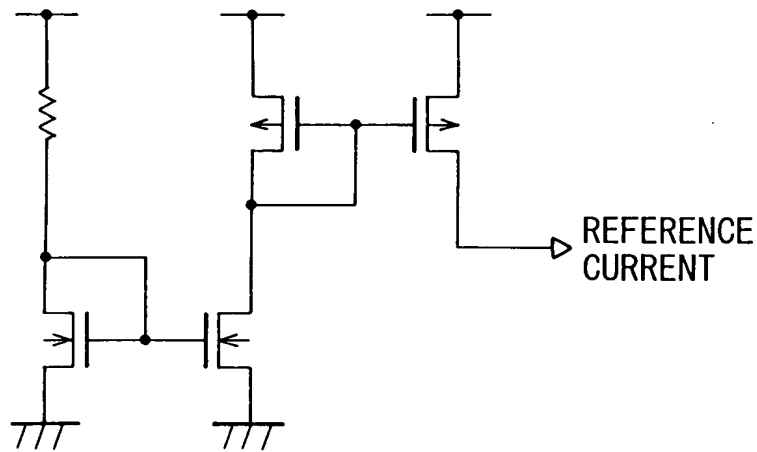


FIG. 41

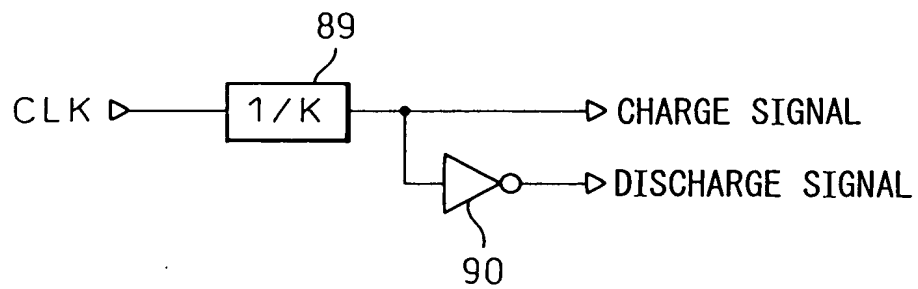


FIG. 42

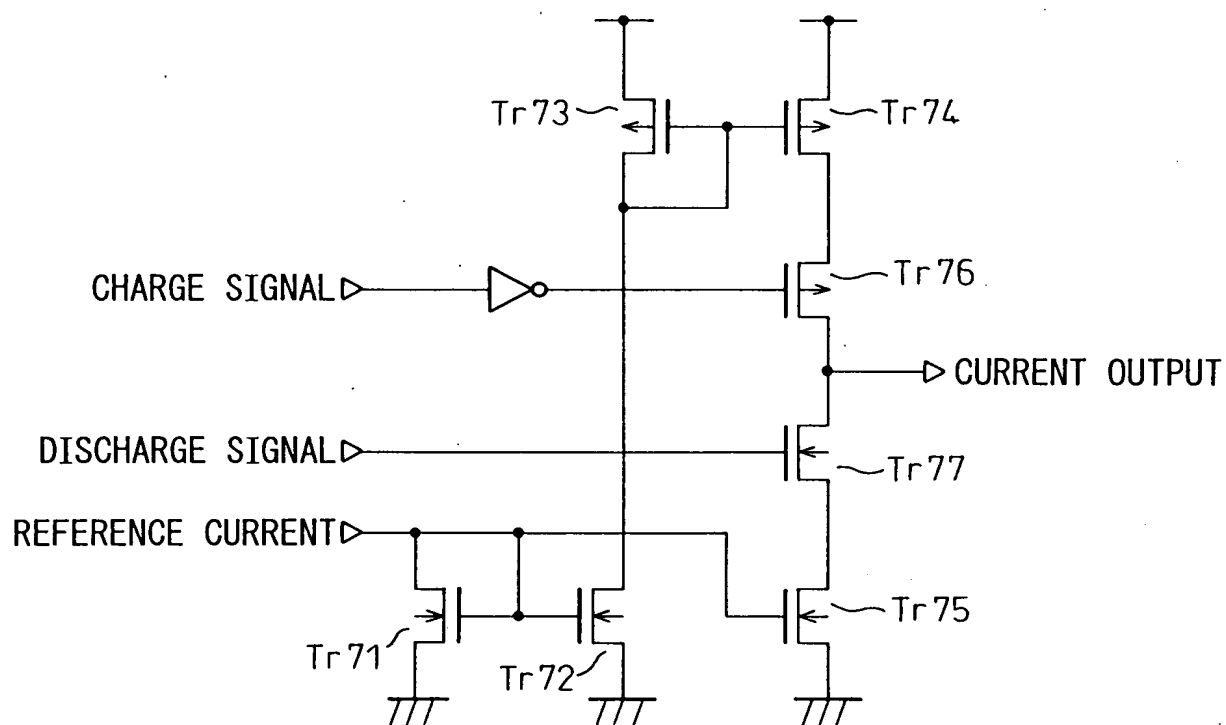


FIG. 43A

CHARGE SIGNAL

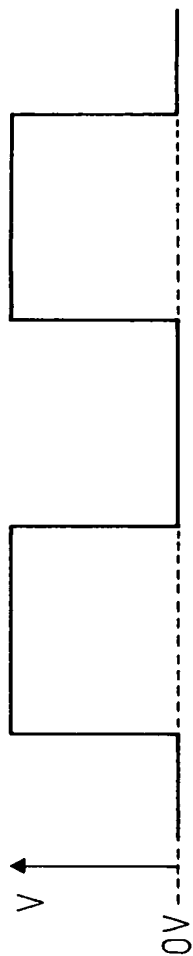


FIG. 43B

DISCHARGE SIGNAL

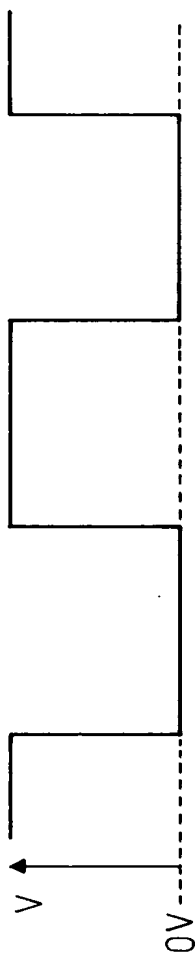


FIG. 43C

CURRENT OF THE
 CURRENT SOURCE

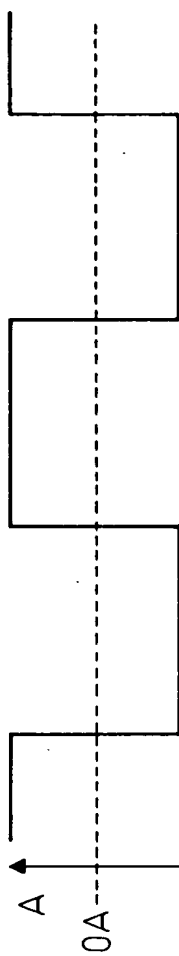


FIG. 43D

VOLTAGE OF A NODE
 BETWEEN A RESISTOR
 AND A CAPACITOR OF
 A LOOP-FILTER
 (VCO CONTROL
 VOLTAGE)

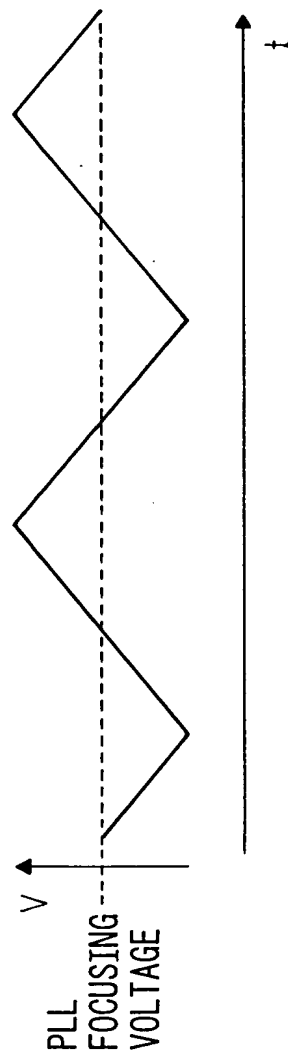


FIG. 44

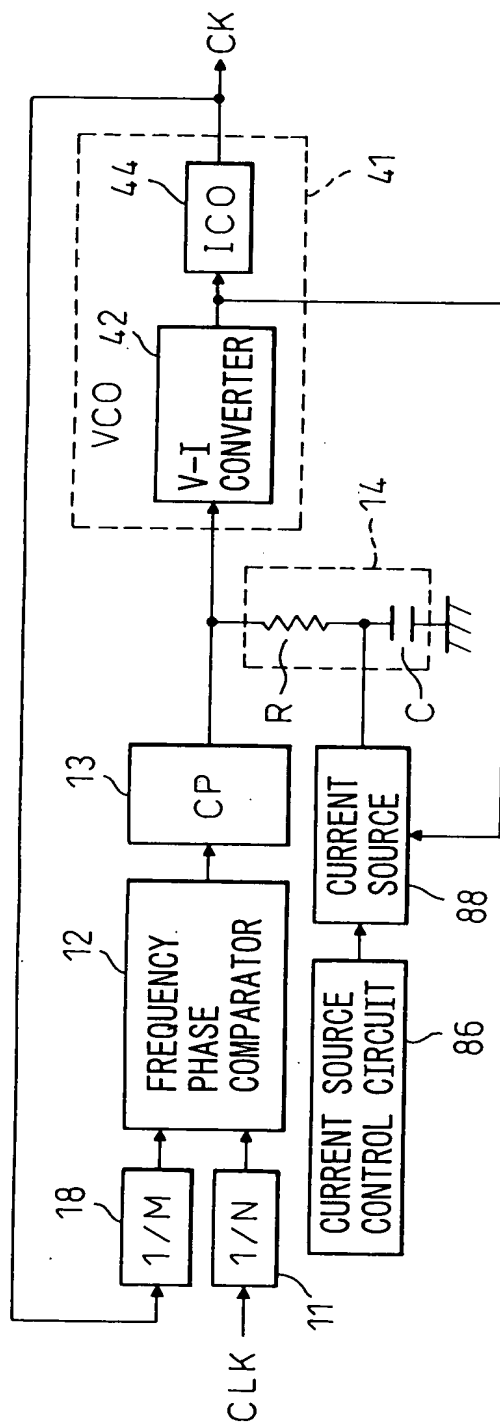


FIG. 45

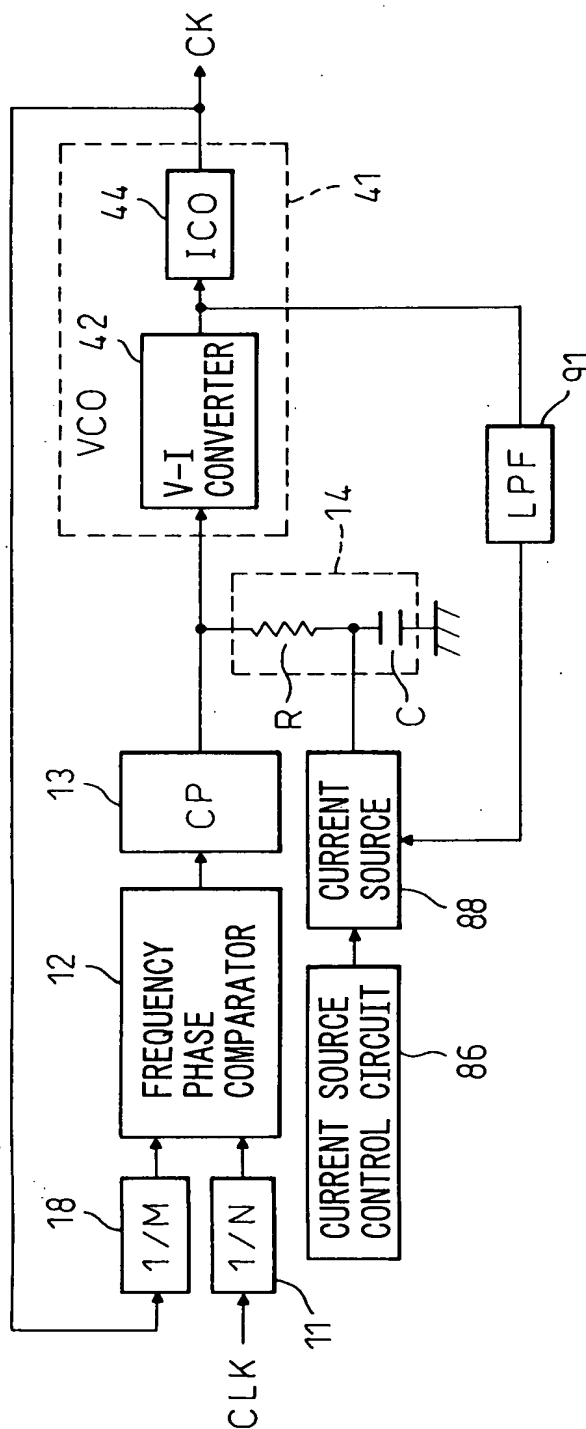


FIG. 46

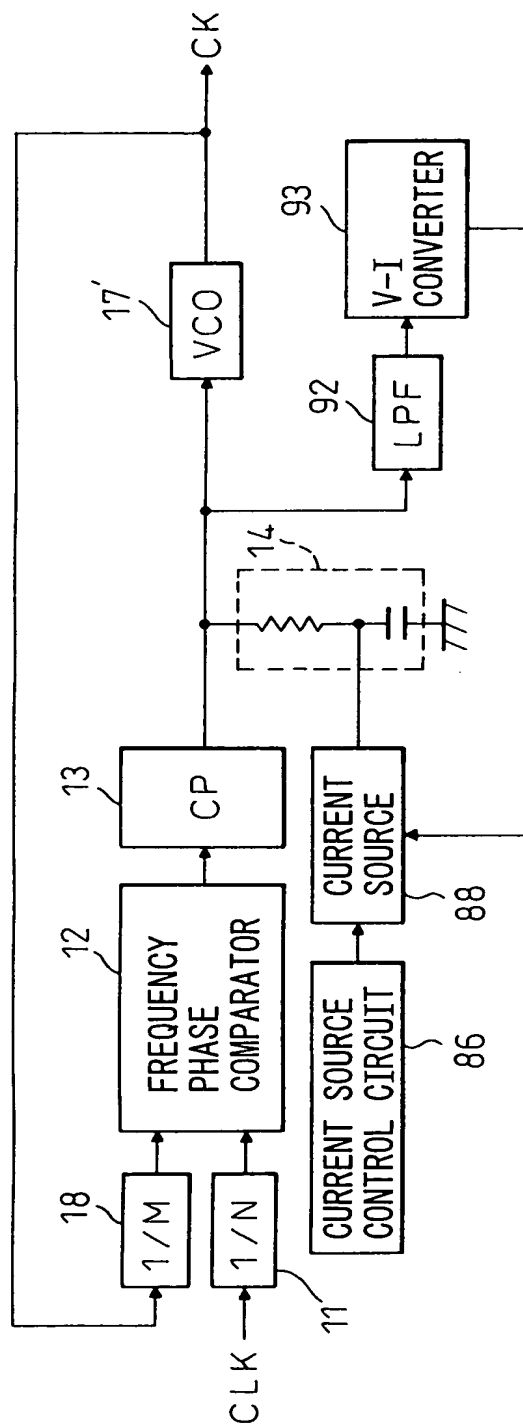


FIG. 47

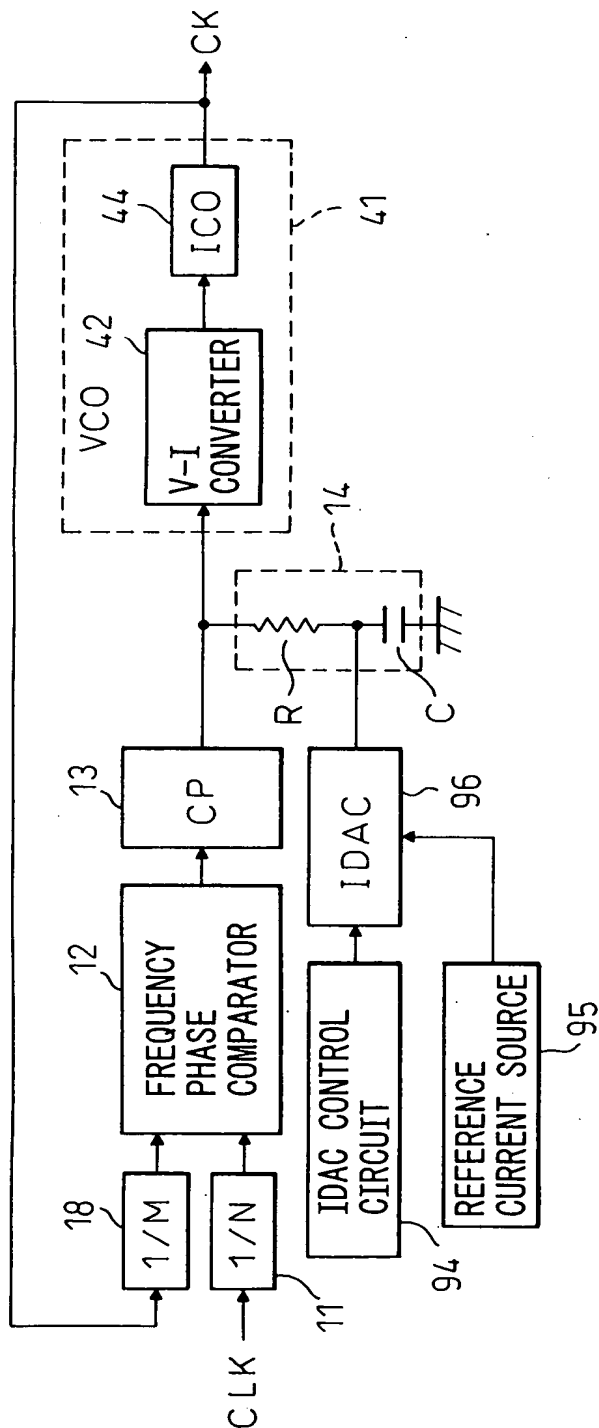


FIG. 48

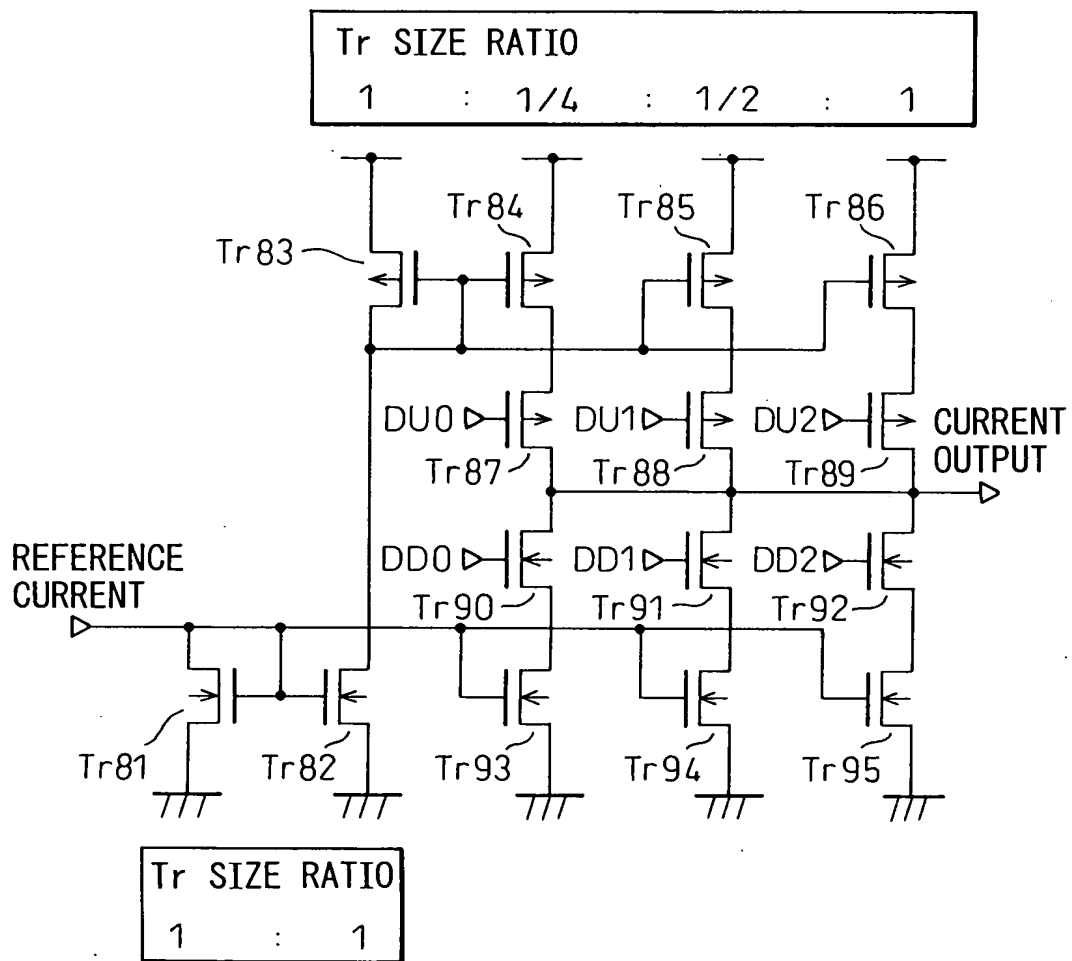


FIG. 49A FIG. 49B FIG. 49C

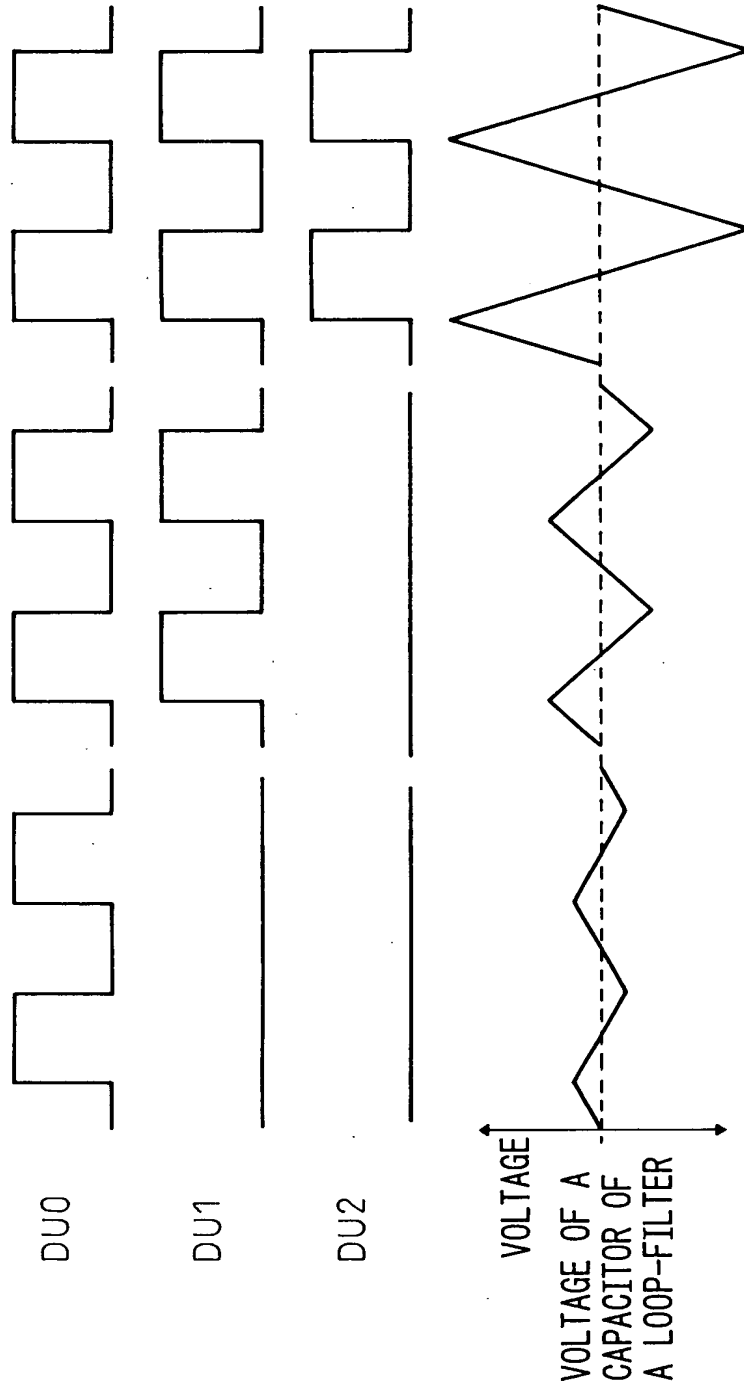


FIG.50

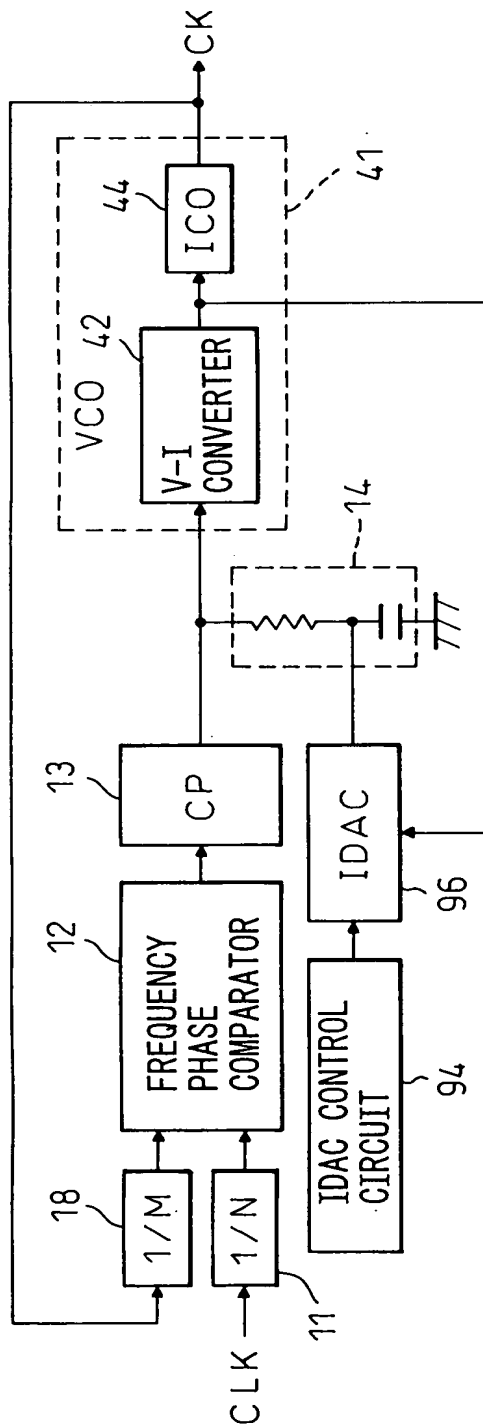


FIG. 51

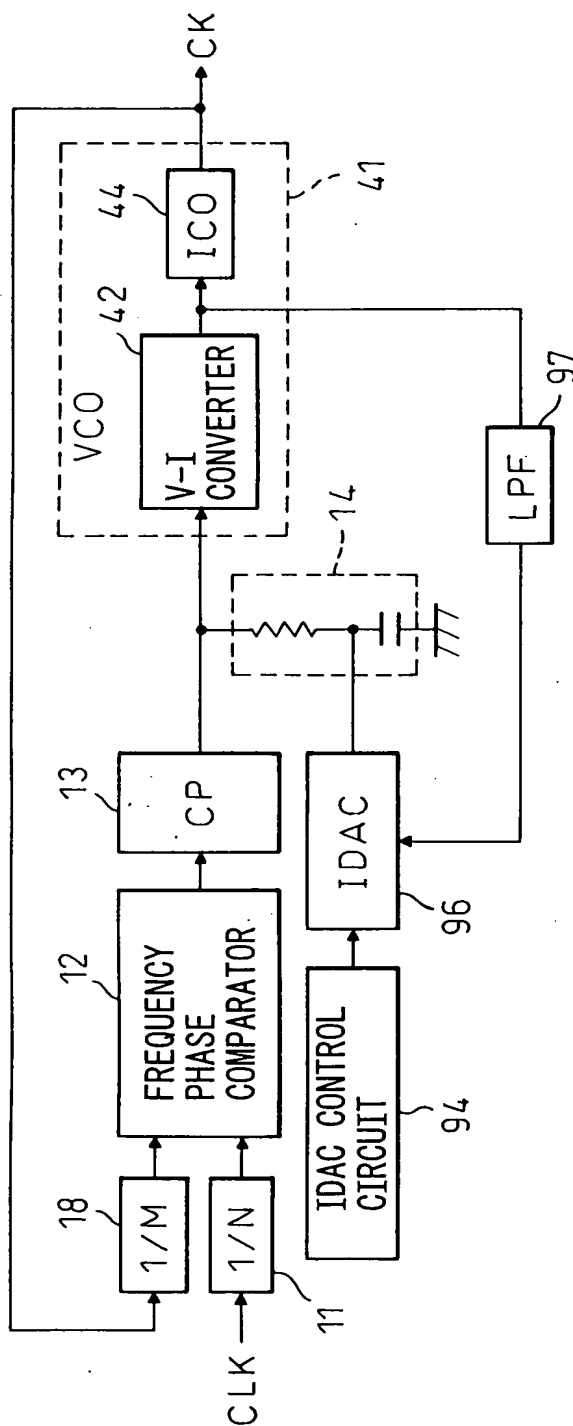


FIG. 52

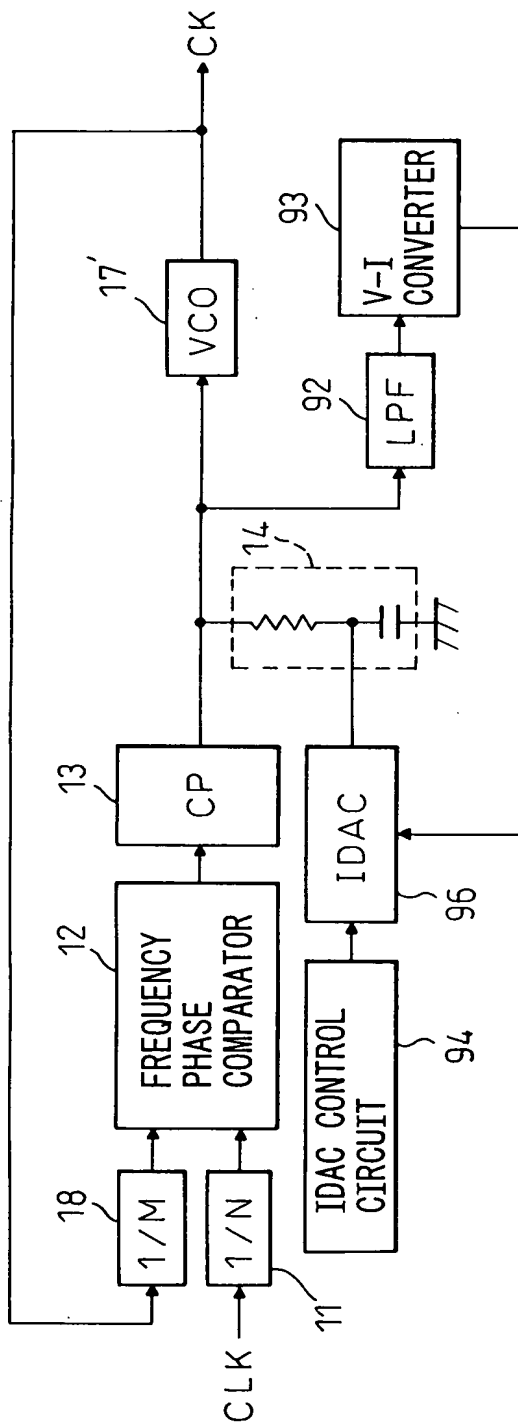


FIG. 53

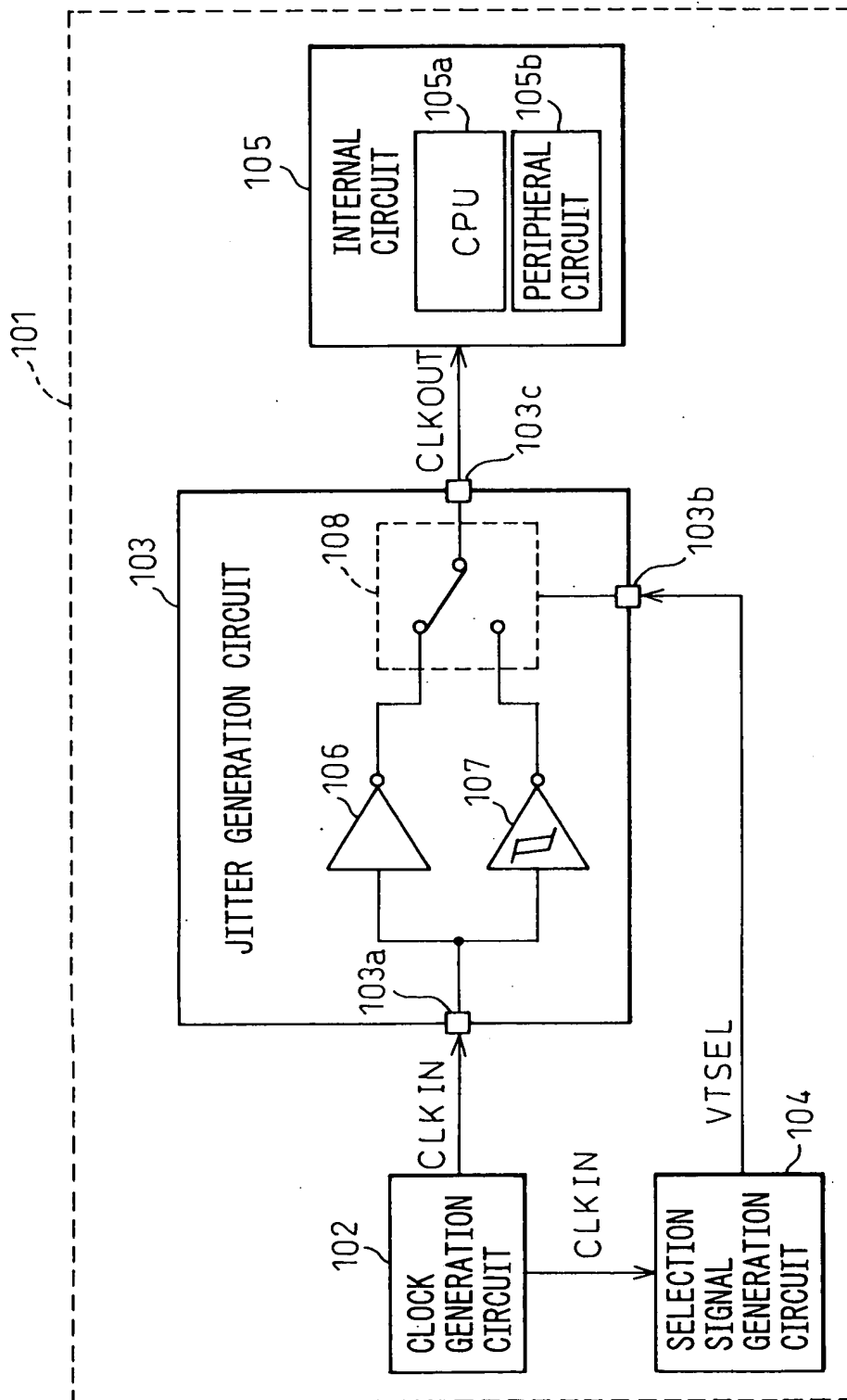


FIG. 54

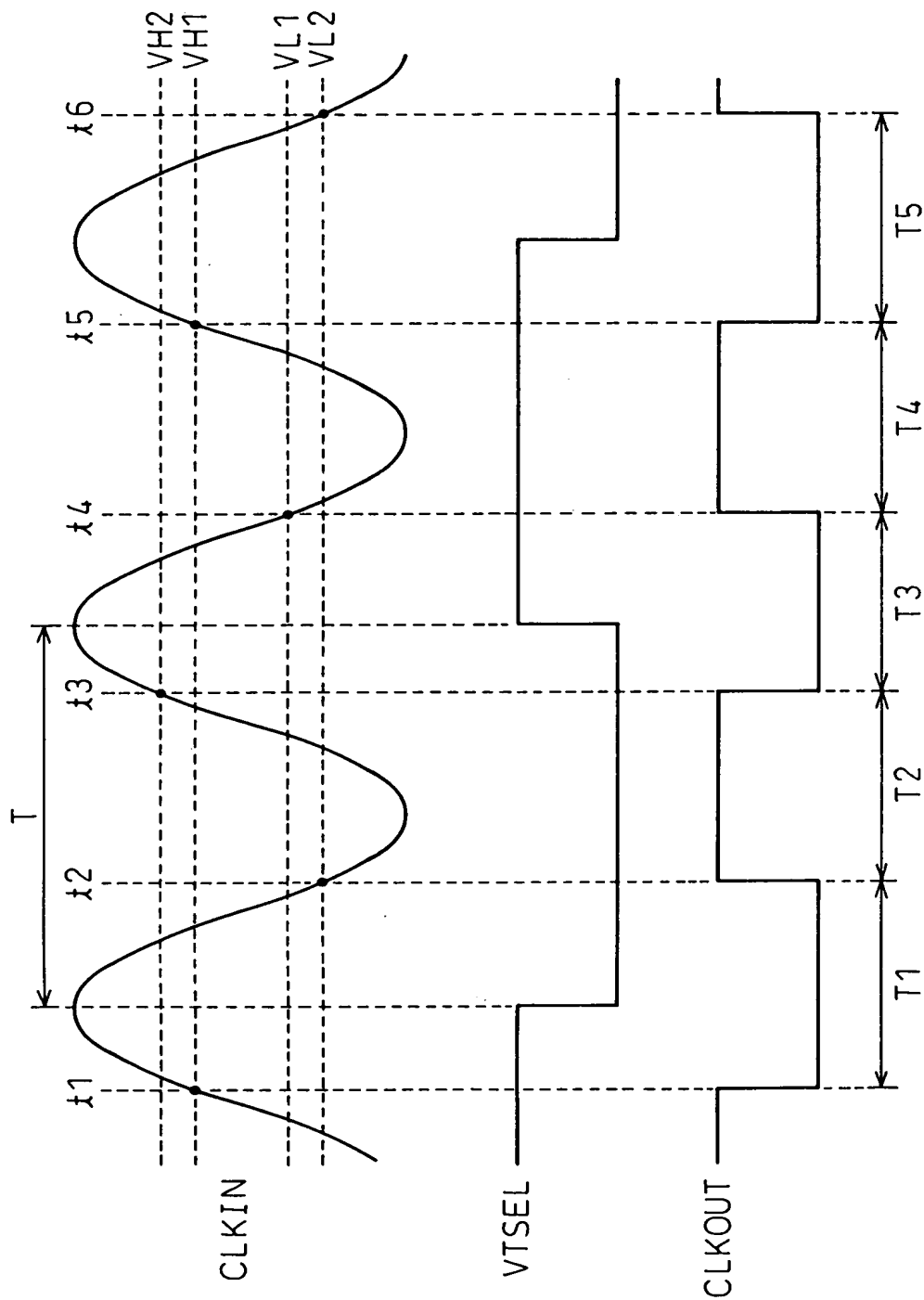


FIG.55

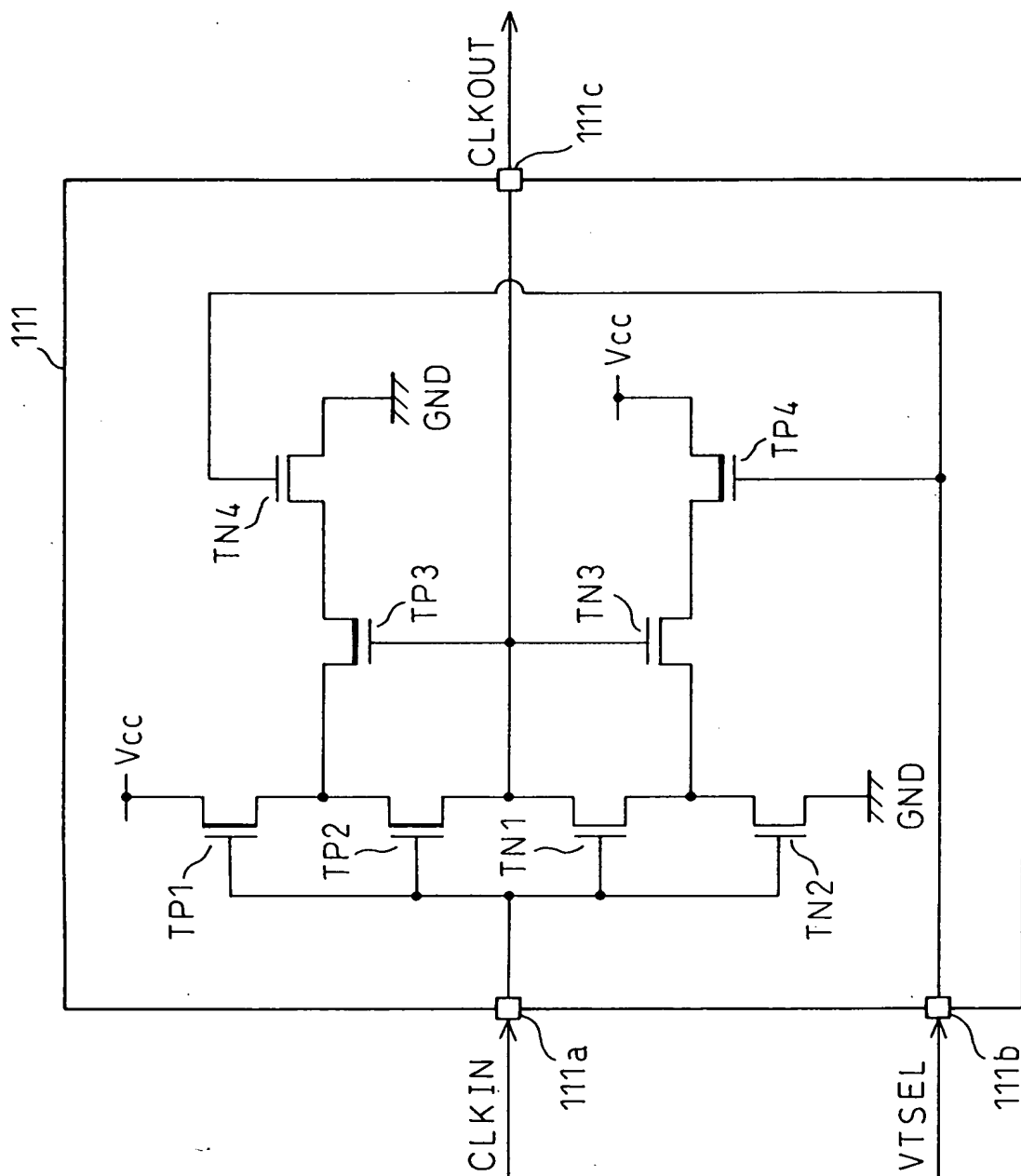
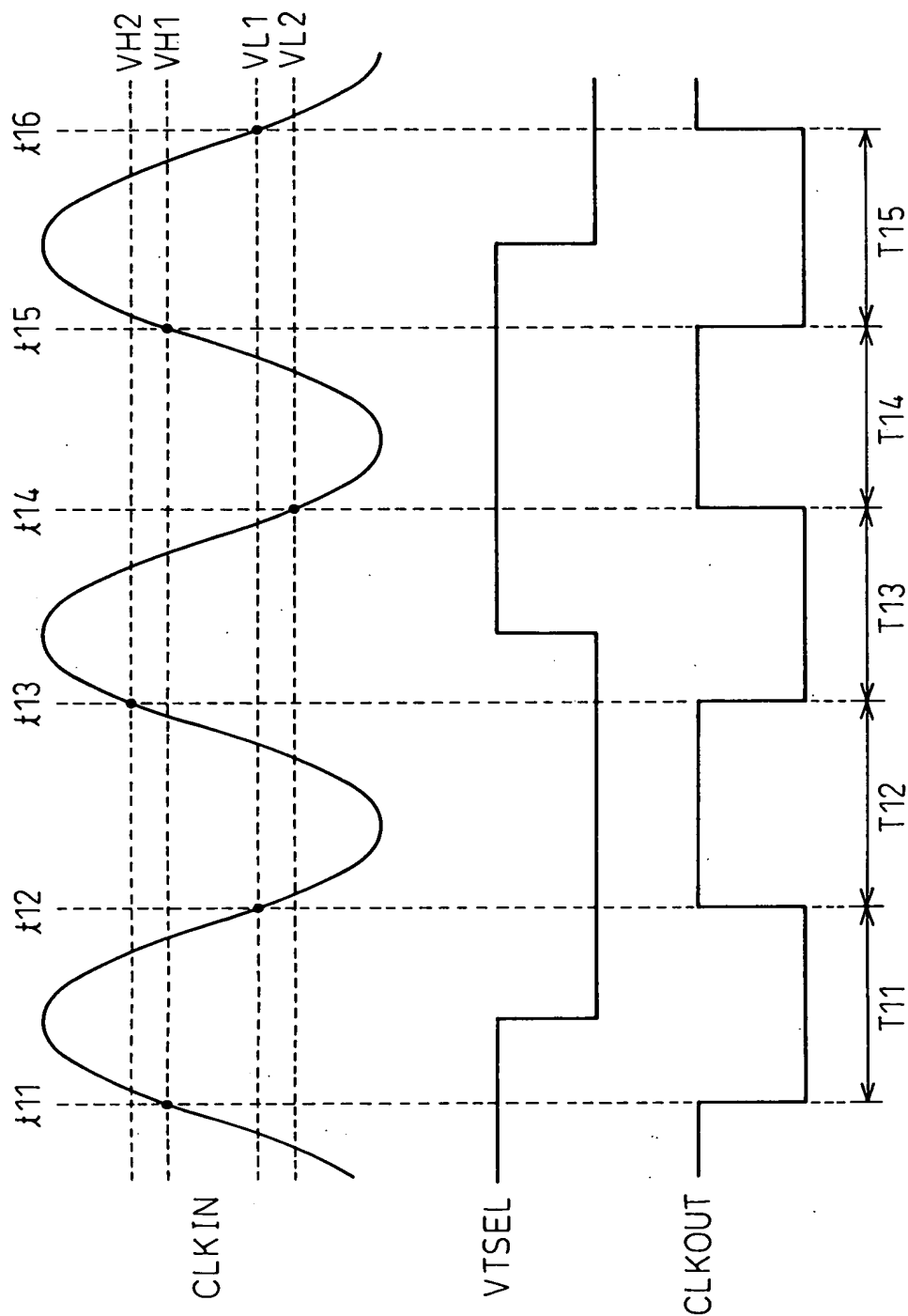


FIG.56



54/62

FIG. 57

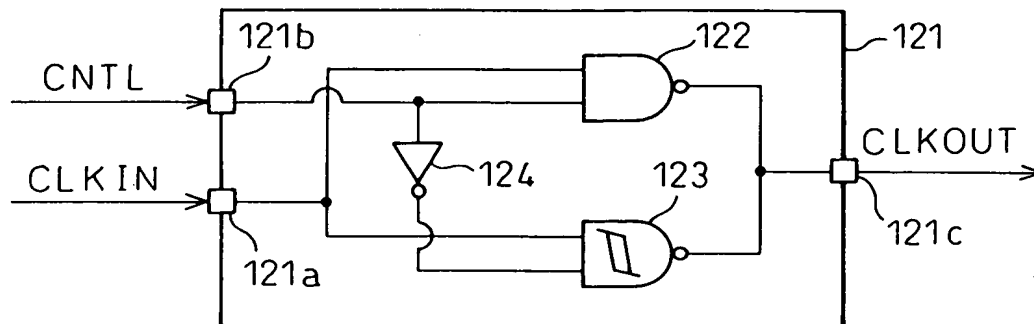


FIG. 58

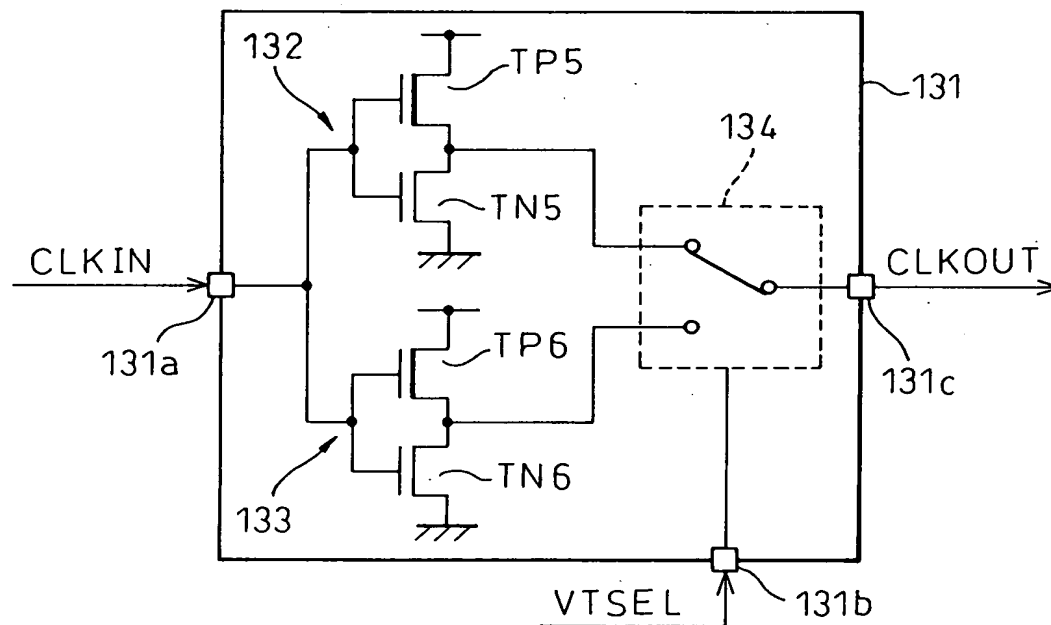


FIG. 59

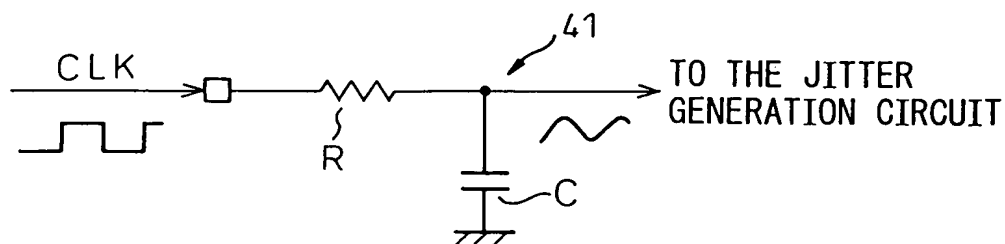


FIG. 60

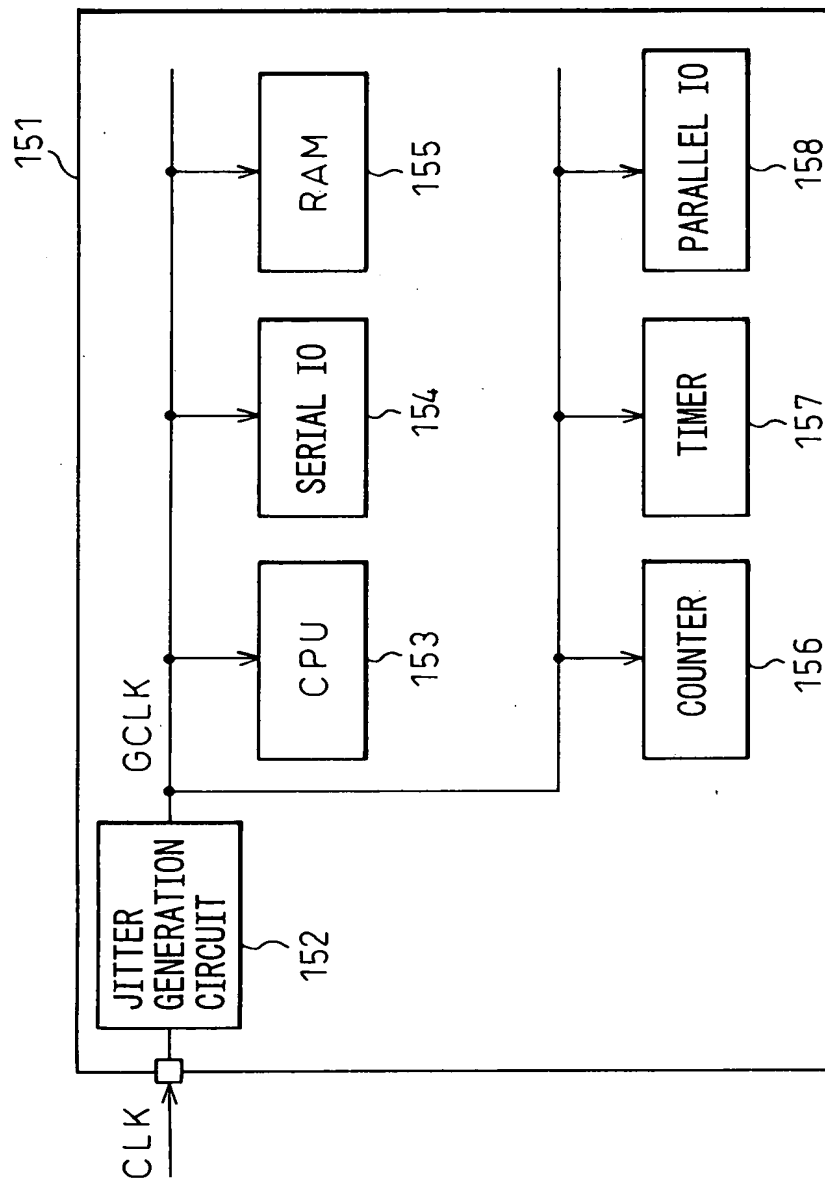


FIG. 61

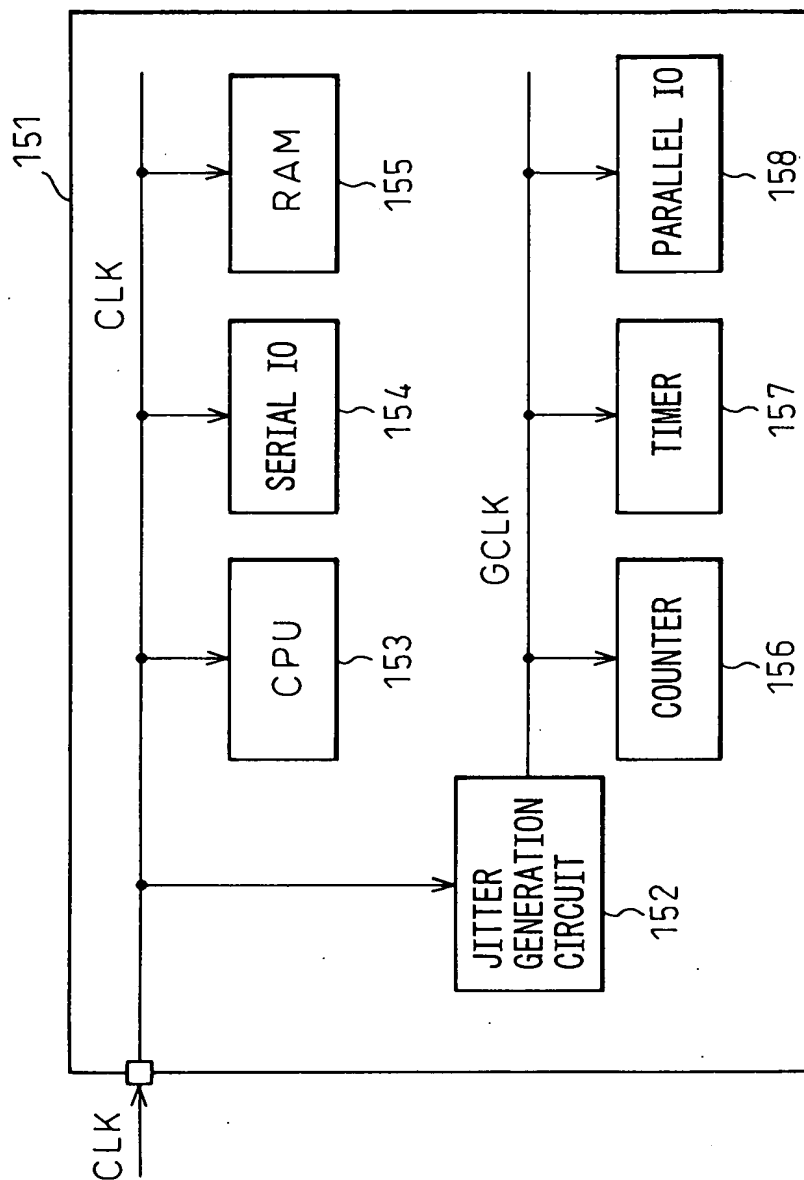


FIG. 62

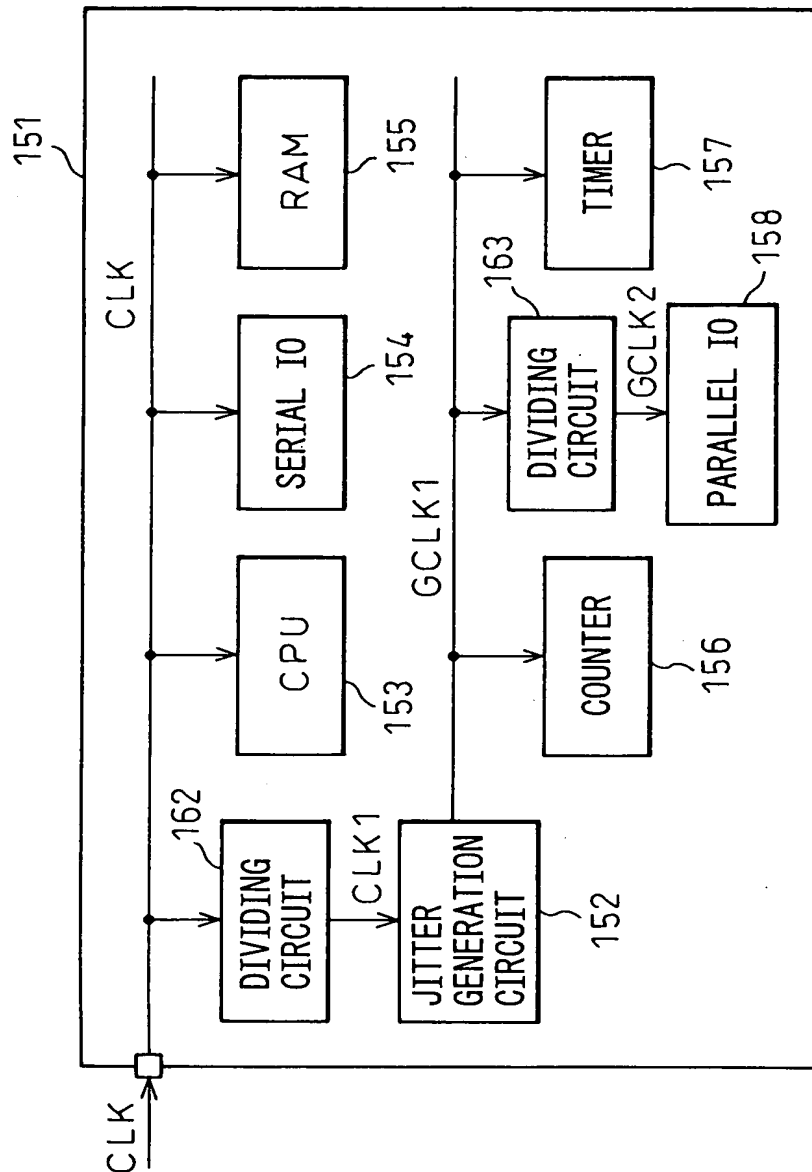


FIG. 63

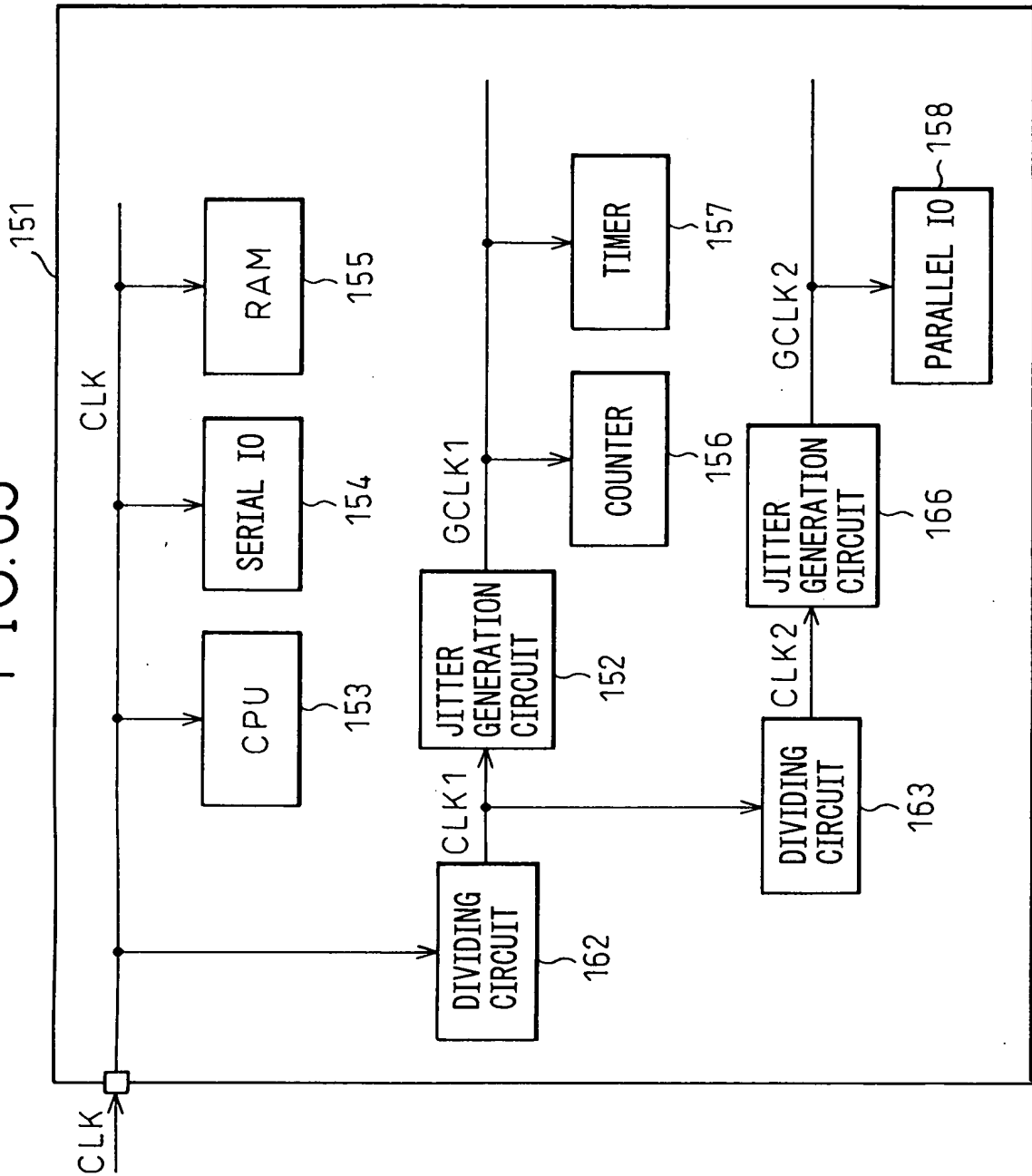


FIG. 64

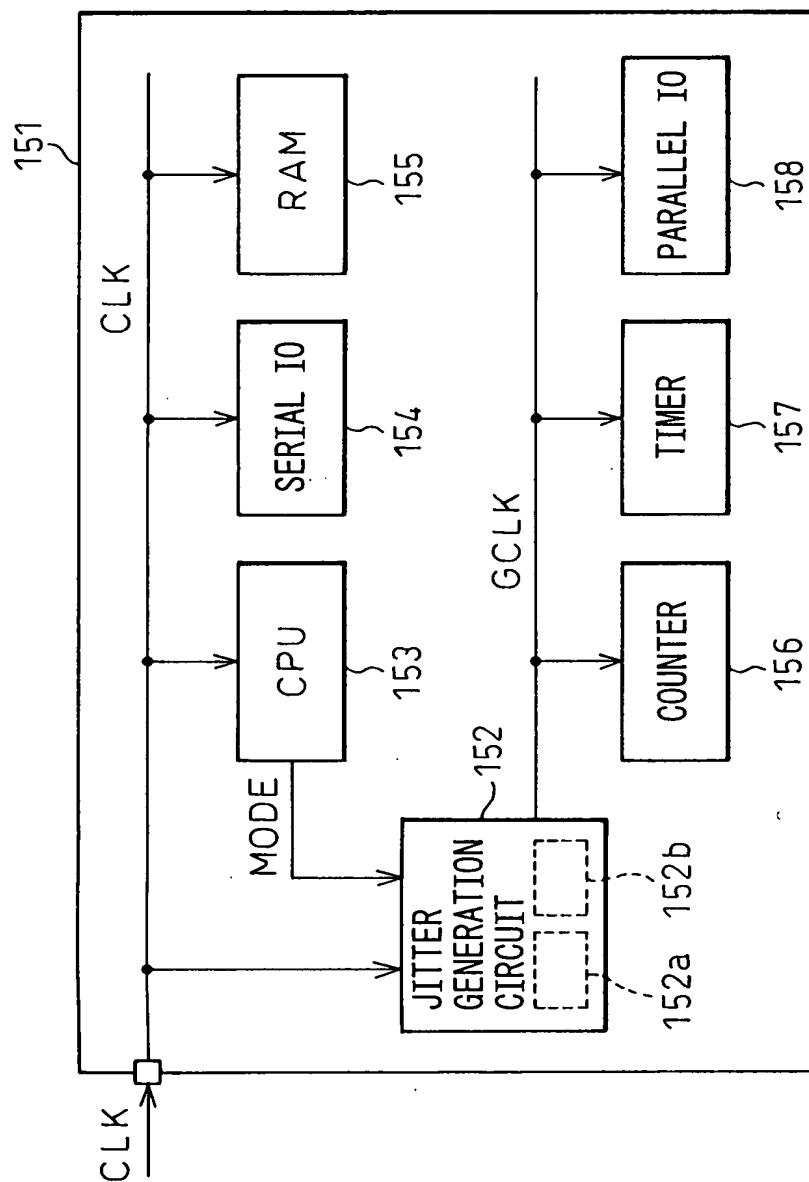


FIG. 65

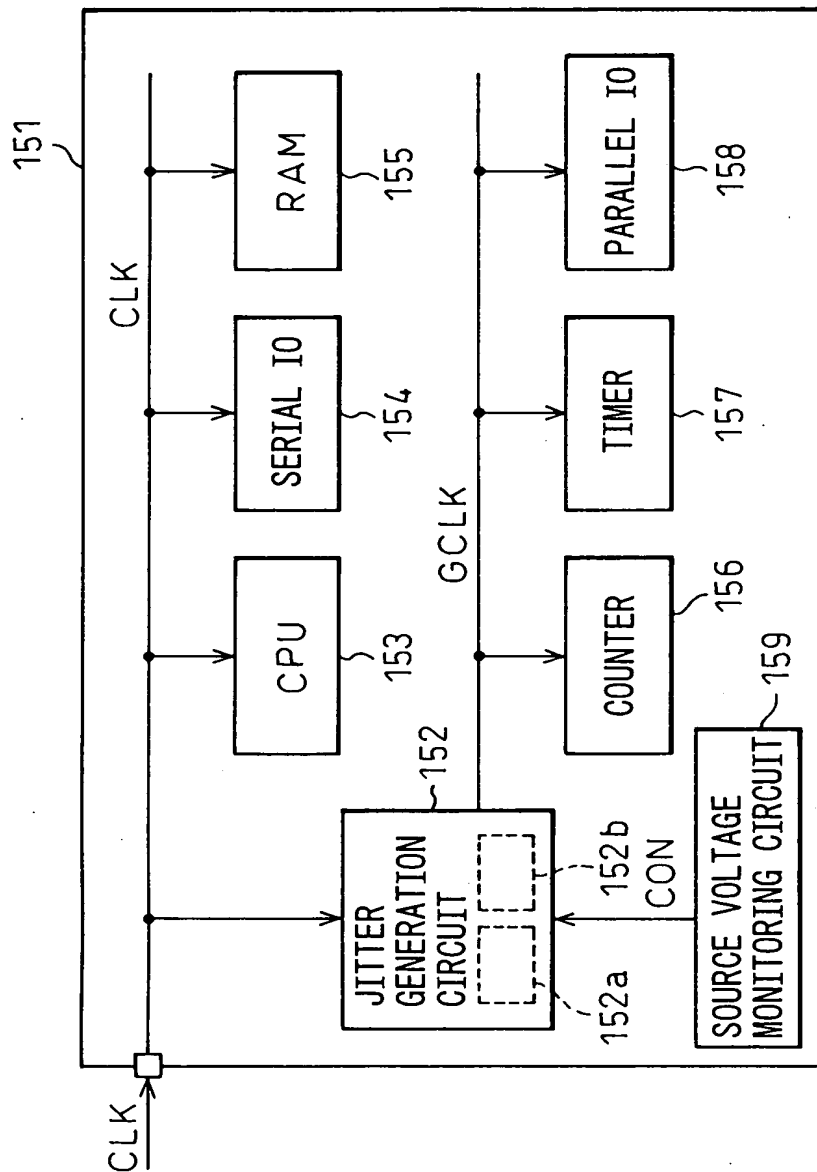


FIG. 66

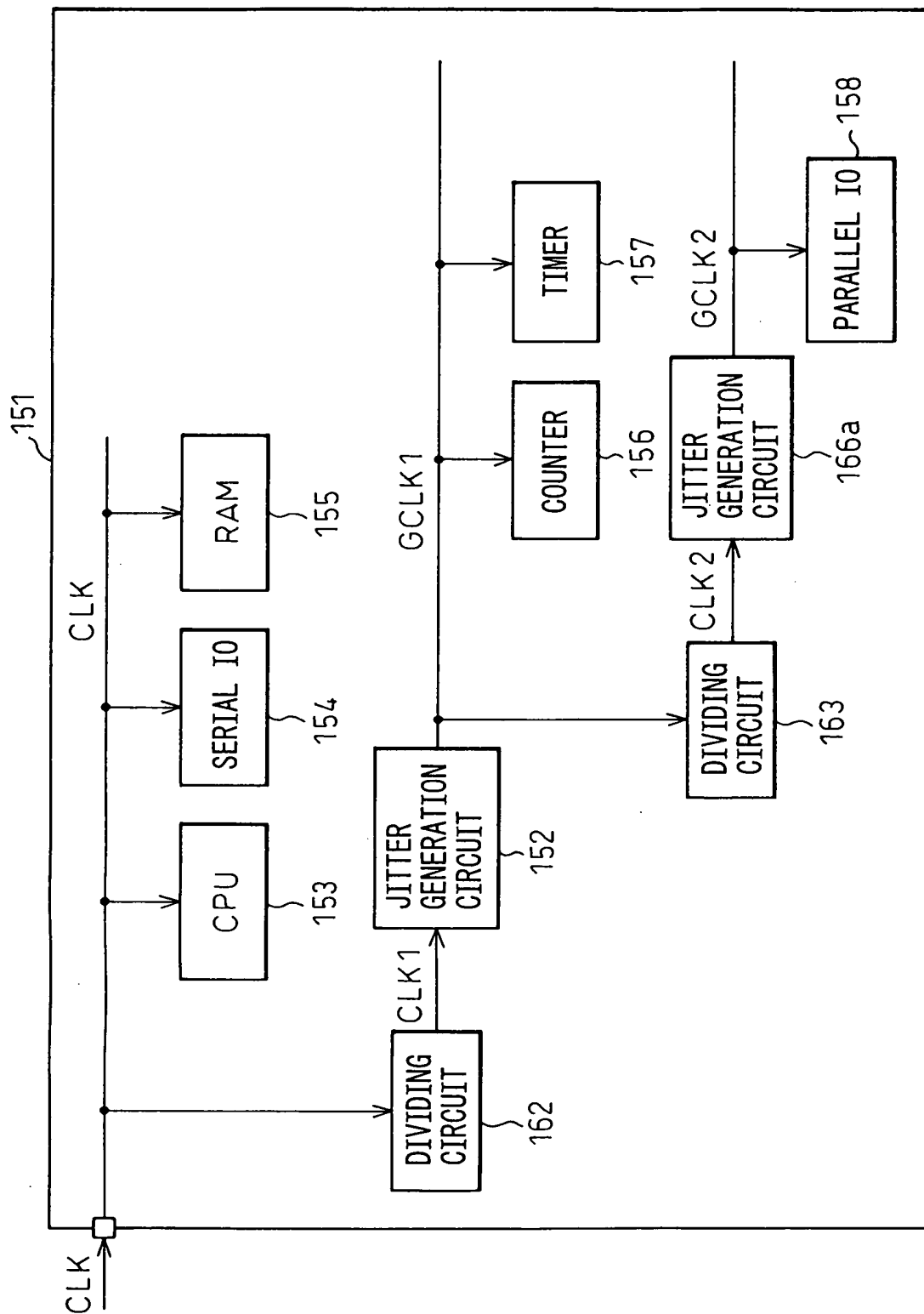


FIG. 67

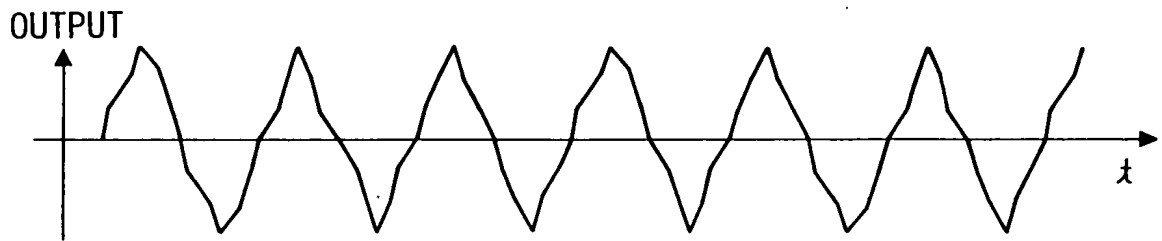


FIG. 68

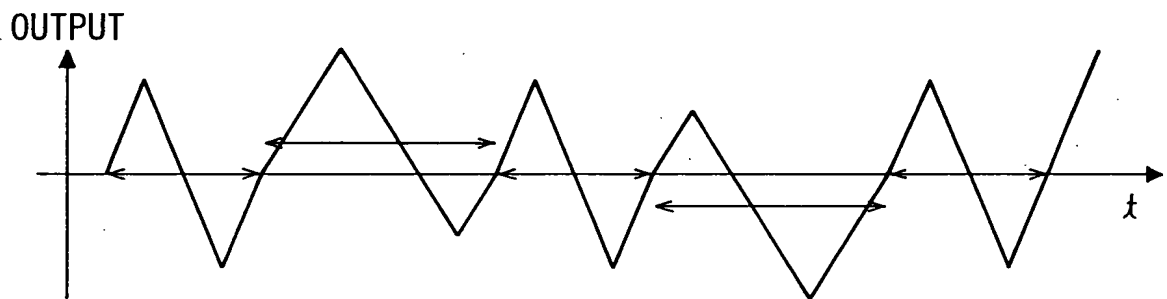


FIG. 69

